


TE0763 USB Controller

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This page intends to give minimal documentation on how to set up the connection between the PC and the USB Controller of the TE0763 mounted on a TE0303-01. It describes

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The documentation for older variants like TE0630 can still widely be used as a [reference](#) but the old software might not work like expected with TE0763.



Note that the following linked software is adopted from the original TE USB Suite and is limited to 24 bit addressing. To write to the address space above 24 Bit, adjustments to driver / software and firmware have to be made.

OpenFutNet and Firmware modified for TE0763: Download [TE-USB-Suite-master](#)

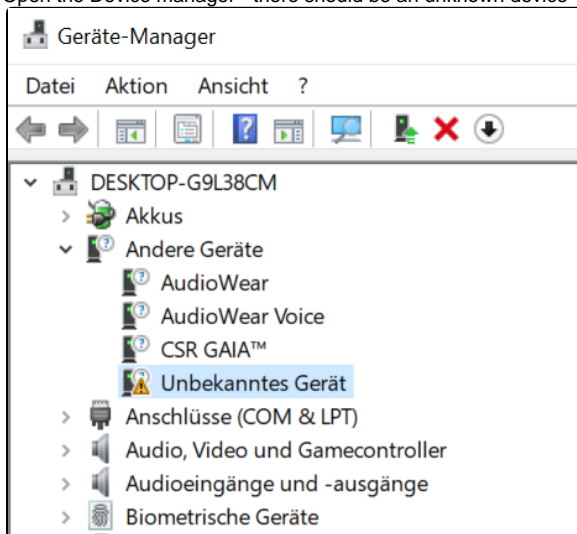
Driver: Download [FX2-Driver](#) for Windows

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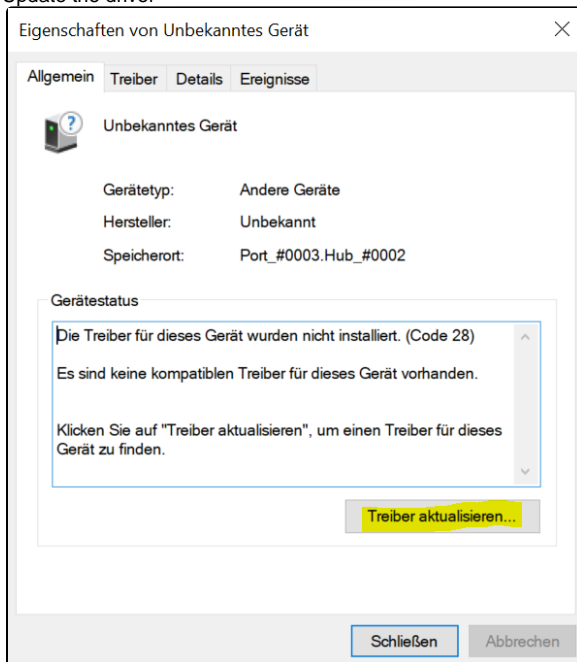
Install the FX2 Driver on Windows10

1. Connect the TE0763 via USB and power on the Board mounted on the carrier

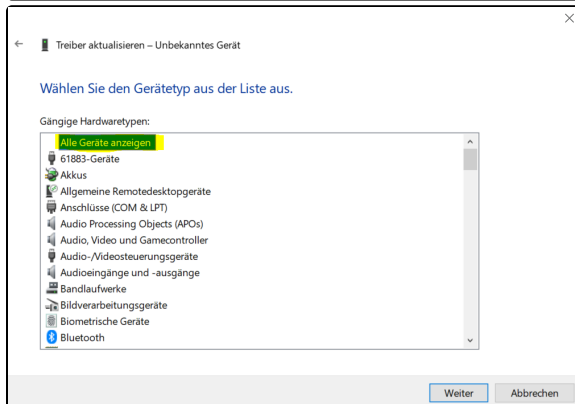
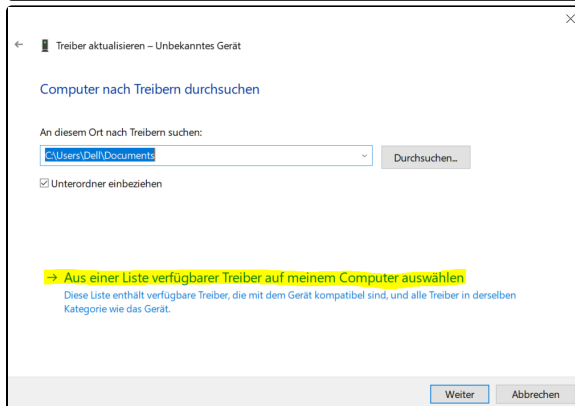
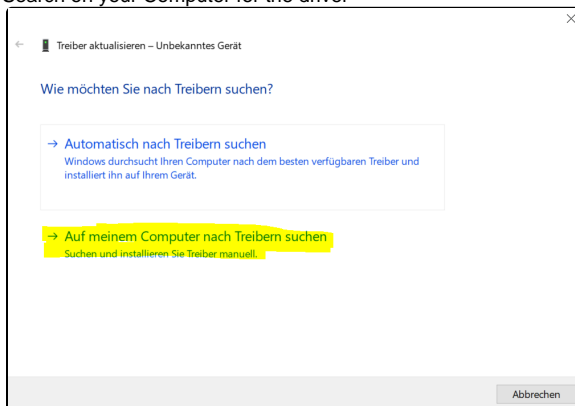
2. Open the Device manager - there should be an unknown device

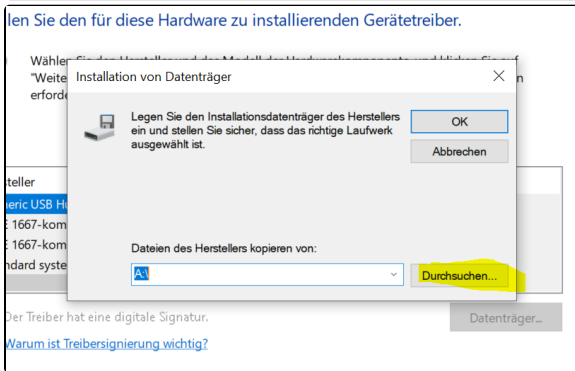
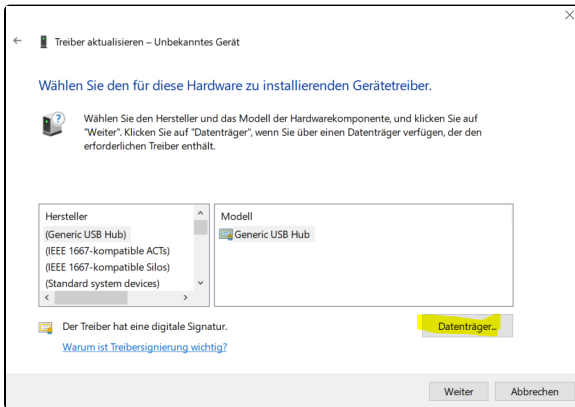


3. Update the driver

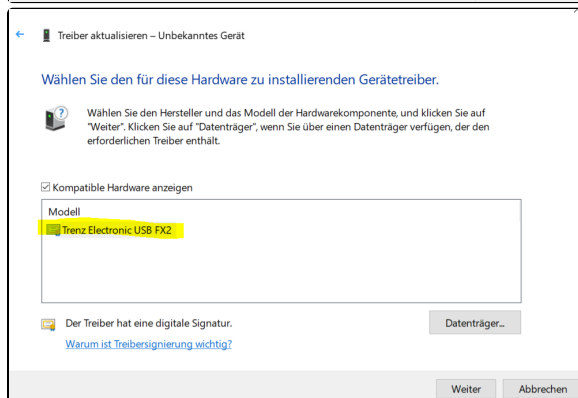
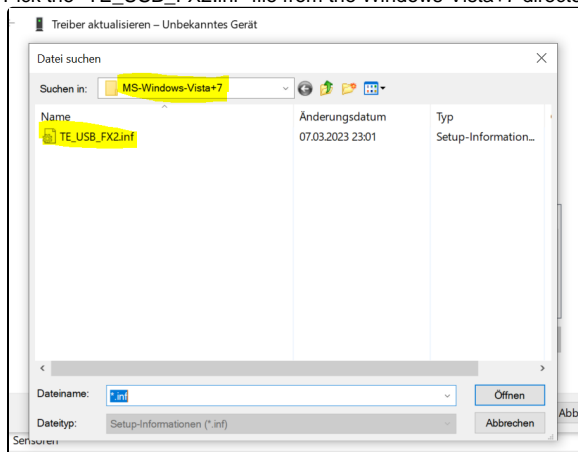


4. Search on your Computer for the driver

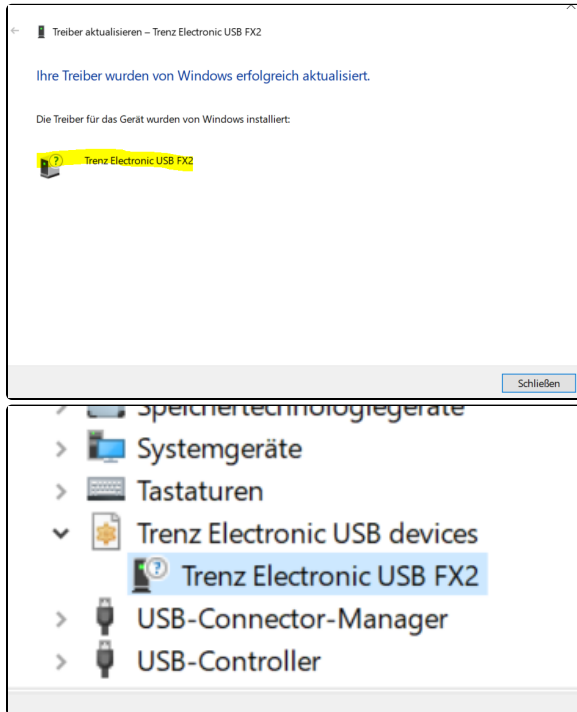




5. Pick the "TE_USB_FX2.inf" file from the Windows-Vista+7 directory

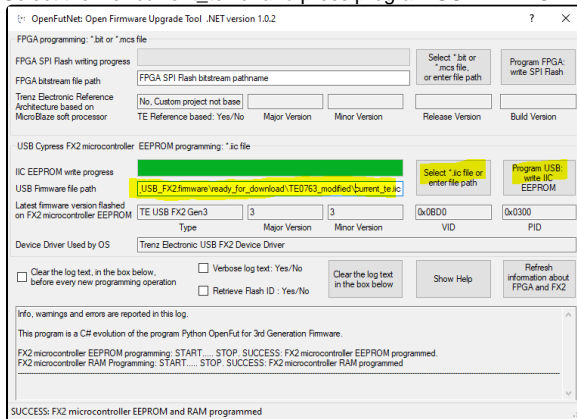


6. The driver should be installed now!



Flash the EEPROM with the modified Firmware

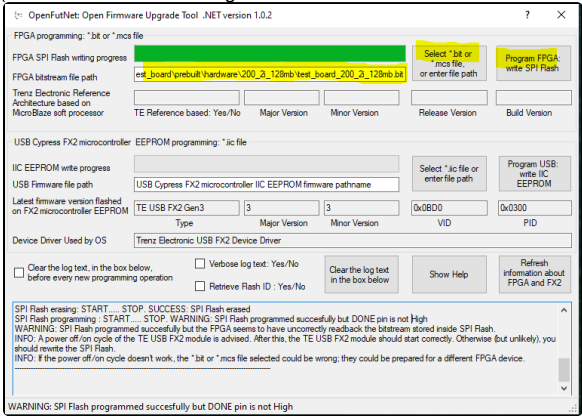
1. Set the DIP-Switch S2A to ON.
2. Connect the Mini USB cable to the module
3. Turn on the Board
4. Open the Software "OpenFutNet.exe" from ...\\TE-USB-Suite-master\\TE_USB_FX2.gen_3\\OpenFutNet\\OpenFutNet\\bin\\Release
5. You may see the default VID and PID different to 0x0BD0 and 0x0300
6. Select the file "current_te.iic" and press program USB EEPROM



7. When this step is successful, you should also see the right VID and PID after a power cycle

Program the QSPI Flash with OpenFutNet(Bitfile only!)

- 1. Set the DIP-Switch S2A and S2B to ON.
- 2. Turn on the Board
- 3. Open the Software "OpenFutNet.exe" from ...\\TE-USB-Suite-master\\TE_USB_FX2.gen_3\\OpenFutNet\\OpenFutNet\\bin\\Release
- 4. Select a Bitfile and press Program FPGA. This step takes a while! The progress bar might not be accurate! The Warning about "DONE Pin" can be ignored.
(info: the Bitfile has to be generated with SPI X1 mode set in the constraint files)



- 5. When successful, after a power cycle the new bitfile should be loaded into the FPGA.

App. A: Change History and Legal Notices

Document Change History

To get content of older revision go to "Change History" of this page and select older document revision number.

Date	Document Revision	Authors	Description
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Document change history.

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Please also note our data protection declaration at <https://www.trenz-electronic.de/en/Data-protection-Privacy>

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Users of electrical and electronic equipment in private households are required not to dispose of waste electrical and electronic equipment as unsorted municipal waste and to collect such waste electrical and electronic equipment separately. By the 13 August 2005, Member States shall have ensured that systems are set up allowing final holders and distributors to return waste electrical and electronic equipment at least free of charge. Member States shall ensure the availability and accessibility of the necessary collection facilities. Separate collection is the precondition to ensure specific treatment and recycling of waste electrical and electronic equipment and is necessary to achieve the chosen level of protection of human health and the environment in the European Union. Consumers have to actively contribute to the success of such collection and the return of waste electrical and electronic equipment. Presence of hazardous substances in electrical and electronic equipment results in potential effects on the environment and human health. The symbol consisting of the crossed-out wheeled bin indicates separate collection for waste electrical and electronic equipment.

Trenz Electronic is registered under WEEE-Reg.-Nr. DE97922676.

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proxy279.\$Proxy4022#hasContentLevelPermission. Cannot resolve which method to invoke for [null, class java.lang.String, class com.atlassian.confluence.pages.Page] due to overlapping prototypes between: [interface com.atlassian.confluence.user.

ConfluenceUser, class java.lang.String, class com.atlassian.confluence.core.

ContentEntityObject] [interface com.atlassian.user.User, class java.lang.String, class
com.atlassian.confluence.core.ContentEntityObject]