TE0763 USB Controller

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This page intends to give minimal documentation on how to set up the connection between the PC and the on Bod & USB CController of the TE0763 mounted on a TE0303-01. It describes

- 2.1 Install the FX2 Driver on Windows10
- how to 2nd talk that TRENZ RION in win 100 PGd Firmware
- how to 21 as Pither & E RROM 6 Ph Rhes TE 0763 with the be (Wiffiren waye)
- 3 Apply to pobgage this QSP and sound hit is open FutNet Software
 - 3.1 Document Change History

The documentation life paldeo wariants like TE0630 can still widely be used as a reference but the old software might not work like expected with TE0763.

- 3.4 Document Warranty
- ° 3.5 Limitation of Liability
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- 3.7 Technology Licenses
 Notedhat the following lighed software is adopted from the original TE USB Suite and is limited by 34 bit addressing To the address space above 24 Bit, adjustments to driver
 4/pattware and firmware have to be made.

OpenFutNet and Firmware modified for TE0763: Download TE-USB-Suite-master

Driver: Download FX2-Driver for Windows

USB Chip Contoller

Install the FX2 Driver on Windows10

1. Connect the TE0763 via USB and power on the Board mounted on the carrier

2. Open the Device manager - there should be an unknown device



4. Search on your Computer for the driver

-	
~	📱 Treiber aktualisieren – Unbekanntes Gerät
	Wie möchten Sie nach Treibern suchen?
	→ Automatisch nach Treibern suchen
	Windows durchsucht Ihren Computer nach dem besten verfügbaren Treiber und installiert ihn auf Ihrem Gerät.
	→ Auf meinem Computer nach Treibern suchen
	Suchen und installieren Sie Treiber manuell.
	Abbrechen
-	Treiber aktualisieren – Unbekanntes Gerät
	Computer nach Treibern durchsuchen
	An diesem Ort nach Treibern suchen:
	C:\Users\Dell\Documents
	☑ Unterordner einbeziehen
	→ Aus einer Liste verfügbarer Treiber auf meinem Computer auswählen
	Diese Liste enthält verfügbare Treiber, die mit dem Gerät kompatibel sind, und alle Treiber in derselben Kategorie wie das Gerät.
	Weiter Abbrechen
	;
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	ieren – Unbekanntes Gerat			
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5. Pick the "TE_USB_FX2.inf" file from the Windows-Vista+7 directory

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6. The driver should be installed now!



Flash the EEPROM with the modified Firmware

- 1. Set the DIP-Switch S2A to ON.
- 2. Connect the Mini USB cable to the module
- 3. Turn on the Board
- 4. Open the Software "OpenFutNet.exe" from ...\TE-USB-Suite-master\TE_USB_FX2. gen_3\OpenFutNet\OpenFutNet\bin\Release
 You may see the default VID and PID different to 0x0BD0 and 0x0300
- 6. Select the file "current_te.iic" and press program USB EEPROM

FPGA programming: ".bit or ".mcs	le		
FPGA SPI Rash writing progress	FPGA SPI Rash bitstream pathname	Select "bit or "mcs file, or enter file path	Program FPGA: write SPI Flash
Trenz Electronic Reference Architecture based on MicroBlaze soft processor	No, Custom project not base TE Reference based: Yes/No Major Version Minor Version	Release Version	Build Version
USB Cypress FX2 microcontroller	EEPROM programming: ".ic file		
IC EEPROM write progress JSB Firmware file path	USB_FX2firmware\ready_for_download\TE0763_modfied\burrent_te.lic	Select "Jic file or enterfile path	Program USB: write IIC EEPROM
atest firmware version flashed on FX2 microcontroller EEPROM	TE USB FX2 Gen3 3 3 Type Major Version Minor Version	0x0BD0	0x0300 PID
Device Driver Used by OS	Trenz Electronic USB FX2 Device Driver		
Clear the log text, in the box l before every new programmin	alow. Verbose log text: Yes/No Clear the log text in the box below in the box below	Show Help	Refresh information about FPGA and FX2
Info, warnings and errors are report This program is a C# evolution of FX2 microcontroller EEPROM pro	ted in this log. he program Python Open Fut for 3rd Generation Firmware. pramming STARTSTOP_SUCCESS.FX2 microcontroller EEPROM prog	grammed.	

7. When this step is successful, you should also see the right VID and PID after a power cycle

Program the QSPI Flash with OpenFutNet(Bitfile only!)

- 1. Set the DIP-Switch S2A and S2B to ON.
- 2. Turn on the Board
- 3. Open the Software "OpenFutNet.exe" from ...\TE-USB-Suite-master\TE_USB_FX2. gen_3\OpenFutNet\OpenFutNet\bin\Release
- Select a Bitfile and press Program FPGA. This step takes a while! The progress bar might not be accurate! The Warning about "DONE Pin" can be ignored.
 - (info: the Bitfile has to be generated with SPI X1 mode set in the constraint files)



5. When successful, after a power cycle the new bitfile should be loaded into the FPGA.

App. A: Change History and Legal Notices

Document Change History

To get content of older revision go to "Change History" of this page and select older document revision number.

Date	Document Revision	Authors	Description
			initial release
Error	Error	Error	
renderi	renderi	renderi	
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Document change history.

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Trenz Electronic is registered under WEEE-Reg.-Nr. DE97922676.

Error rendering macro 'page-info'

Ambiguous method overloading for method jdk.

proxy279.\$Proxy4022#hasContentLevelPermission. Cannot resolve which method to

invoke for [null, class java.lang.String, class com.atlassian.confluence.pages.Page] due

to overlapping prototypes between: [interface com.atlassian.confluence.user.

ConfluenceUser, class java.lang.String, class com.atlassian.confluence.core. ContentEntityObject] [interface com.atlassian.user.User, class java.lang.String, class com.atlassian.confluence.core.ContentEntityObject]