

TEB0911 Reference Designs

Description for the newest Design Version

- [TEB0911 Test Board](#) -TEB0911 UltraScale+ ZynqMP Basic-System
 - Vitis/Vivado 2019.2
 - PetaLinux
 - SD
 - ETH
 - MAC from EEPROM
 - USB
 - I2C
 - PCIe
 - DP
 - FMeter
 - LED
 - Modified FSBL for SI5338 and SI5345 programming
 - Special FSBL for QSPI programming

Basic Documentation and Notes

- [AMD Development Tools](#)
 - [TE Board Part Files](#)
 - [TE Board Part Installation Options](#)
- [PetaLinux KICKstart](#)
- [TE Reference Design - Project Delivery](#)

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