

TEC0117 TRM

Key Features

- GOWIN LittleBee 1NR9 FPGA
 - 8MByte internal SDRAM
- 8Mbyte SPI Flash for user applications
- JTAG and UART over Micro USB2 connector
- 1x6 pin header holes for direct JTAG access
- 1x Pmod header providing 8 I/O
- 2x 14-pin headers (2,54 mm pitch) providing 22 I/O
- 8x user LEDs
- 1x user push button
- 100MHz user clock (MEMS oscillator)
- 5.0V single power supply with on-board voltage regulators
- Size: 61.5 x 25 mm