

7 Series Kintex and Artix based modules

	TE	0741	-	03	-	x	x	x	x	x	x	-	x				
	1 2	3456	7	8 9	10	11	12	13	14	15	16	17	18				
Module type														Additional options. Not available in TE0725LP			
TE	BB Mezzanine Module														no extra options		
Module name															S Starter Kit		
0710	0711	0712	0713	0714	0725	0725LP**									K Heat spreader		
Module Revision														Other options (Connectors, custom options)			
01	Revision 1 (initial)														A Standard height		
Device name															L Low profile		
1	2	3	4	5	6	7	8	Artix							... Custom variation, look additional info		
A12	A15	A25	A35	A50	A75	A100	A200	Kintex									
A	B	D	F	G	H	K											
K70	K160	K325	K355	K410	K420	K480											
Speed grade														eMMC () . Field not present in TE0725LP			
1	2	3	4	5										- Not present at all ()			
1	2	3	1L	2L										0 Not populated			
FPGA Temperature range (Tstg abs = -65..150°C)														F			
C				E				I				Q				G	
Commercial (Tj = 0..85°C)				Extended (Tj = 0..100°C)				Industrial (Tj = -40..100°C)				Automotive (Tj = -40..125°C)				H	
DDR memory ()														K			
-	Not present at all													0	Not populated		TE #
0	Not populated													1	256Mbit	S25FL256SAGBI20	#25945
1														2	128Mbit	IS25LP128F-JBLE	#29438
X	SODIMM	Socket												3			
														4			

- TE0725LP is exception. Positions after LP shifted right on two positions. Therefore positions 16 (eMMC) and 18 (Reserve) excluded

Other assembly variations (position 17)

Module		Description
TE0714	A	Standard profile LSHM-150-04.0-L-DV-A-S-K-TR
	L	Low profile LSHM-150-02.5-L-DV-A-S-K-TR
	D	Standard profile, U7 (SPI-FLASH) IS25LP128F-JBLE, U2 (MGT CLOCK) DSC1123CI2-156.2500, R21-R22 not fitted, R27 fitted (VCCIO supply from carrier board)
TE0725LP	T	1.8V variant, connectors soldered (old variant D)
	U	1.8V variant, connectors not mounted (old variant L)
	H	3.3V variant, HyperFlash S26KS512SDPBHI020 #28925

Other assembly variations (position 18)

Module		Description
TE0741	F	Standard profile, FPGA - FFG676 package

