

# TE0820 Reference Designs

## Description for the newest Design Version

- [TE0820 Test Board](#)
  - Vitis/Vivado 2019.2
  - PetaLinux
  - SD
  - ETH
  - USB
  - I2C
  - RTC
  - FMeter
  - MAC from EEPROM
  - User LED (PCB REV03 only)
  - Modified FSBL for SI5338 programming
  - Special FSBL for QSPI programming
- [TE0820 TD TEF1002](#)
  - Vitis/Vivado 2019.2
  - PetaLinux
  - PCIe (endpoint)
  - SATA
  - SD
  - ETH
  - USB
  - I2C
  - RTC
  - FMeter
  - MAC from EEPROM
  - User LED (PCB REV03 only)
  - Modified FSBL for SI5338 programming
  - Special FSBL for QSPI programming
- [TE0820 SDSoC](#)
  - SDSoC platform export
  - SDSoC examples (baremetal and linux)

## Basic Documentation and Notes

- [Xilinx Development Tools](#)
  - [TE Board Part Files](#)
  - [TE Board Part Installation Options](#)
- [PetaLinux KICKstart](#)
- [TE Reference Design - Project Delivery](#)

## Download

- [TE0820 Reference Designs](#)