

TE0803 Reference Designs

Description for the newest Design Version

- [TE0803 Test Board](#)
 - Vitis/Vivado 2019.2
 - QSPI
 - SDK
 - Custom Carrier (minimum PS Design with available module components only)
 - Modified FSBL (some additional outputs only)
 - Special FSBL for QSPI Programming
- [TE0803 StarterKit](#)
 - Vitis/Vivado 2019.2
 - TEBF0808
 - Linux
 - USB
 - ETH
 - MAC from EEPROM
 - PCIe
 - SATA
 - SD
 - I2C
 - RGPIO
 - DP
 - user LED access
 - Modified FSBL for Si5338 programming
 - Special FSBL for QSPI Programming
- [TE0803 SK0803 SDSoC](#)
 - SDSoC platform export
 - SDSoC examples (baremetal and linux)

Basic Documentation and Notes

- [Xilinx Development Tools](#)
 - [TE Board Part Files](#)
 - [TE Board Part Installation Options](#)
- [PetaLinux KICKstart](#)
- [TE Reference Design - Project Delivery](#)

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