

TE0821 Reference Designs

Description for the newest Design Version

Reference Designs	Demo Designs	Tutorial Designs
• TE0821 Test Board ◦ Vitis/Vivado 2022.2 ◦ PetaLinux ◦ SD ◦ ETH ◦ USB ◦ I2C ◦ RTC ◦ FMeter ◦ MAC from EEPROM ◦ User LED ◦ Modified FSBL for SI5338 programming	• TE0821 CR00200 Demo ◦ Vitis/Vivado 2021.2.1 ◦ PetaLinux ◦ SD ◦ FMeter ◦ ETH on CR00200 (J11 on TEB0707-02) ◦ Modified FSBL for SI5338 programming	• TE0821 test board Vitis AI Tutorial ◦ Xilinx 2021.2 tools, Vivado 2021.2.1 ◦ Vitis AI 2.0 ◦ Vitis custom extensible platform for cg02_4gb module ◦ Vector addition on cg02_4gb module ◦ Creation of extensible platform for TE0821 ev04_4gb module by reuse of TE0821 cg02_4gb PetaLinux files ◦ Vitis-AI 2.0 dpu_trd example (ResNet50) with DPU on TE0821 ev04_4gb module
	• TE0821 CR00202 Demo ◦ Vitis/Vivado 2021.2.1 ◦ PetaLinux ◦ SD ◦ ETH on CR00202 (J11 on TEB0707-02) ◦ FMeter ◦ Modified FSBL for SI5338 programming	

Basic Documentation and Notes

- [AMD Development Tools](#)
 - [TE Board Part Files](#)
 - [TE Board Part Installation Options](#)
- [PetaLinux KICKstart](#)
- [TE Reference Design - Project Delivery](#)

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