

TE0320 Related Materials and References

The following documents provide supplementary information useful with this user manual.

Data Sheets

- Xilinx DS485: Digital Clock Manager (DCM) Module Data SheetThis is the data sheet for the Digital Clock Manager (DCM) Module core. www.xilinx.com/support/documentation/ip_documentation/dcm_module.pdf
- Xilinx DS610: Spartan-3A DSP FPGA Family: Complete Data SheetThe Spartan®-3A DSP family of Field-Programmable Gate Arrays (FPGAs) solves the design challenges in most high-volume, cost-sensitive, high-performance DSP applications. www.xilinx.com/support/documentation/data_sheets/ds610.pdf

User Guides

- Xilinx UG331: Spartan-3 Generation FPGA User GuideFunctional description of the Spartan®-3 generation FPGA architecture and how to use it. Includes the Spartan-3A, Spartan-3AN, Spartan-3A DSP, Spartan-3E, and Spartan-3 platforms. www.xilinx.com/support/documentation/user_guides/ug331.pdf
- Xilinx UG332: Spartan-3 Generation Configuration User GuideDescribes the configuration features of the Spartan®-3 Generation FPGAs. Includes the Spartan-3A, Spartan-3AN, Spartan-3A DSP, Spartan-3E, and Spartan-3 FPGA families. www.xilinx.com/support/documentation/user_guides/ug332.pdf
- EZ-USB® Technical Reference Manual (TRM) www.cypress.com/?rID=14667