

TE0630 Clock Oscillators

Two clock oscillators are installed on the TE0630.

Clock oscillator U10 generates a 24 MHz clock signal for the FPGA and the USB controller. This clock signal is used for synchronous communication between FPGA and USB controller.

Clock oscillator U11 generates a 100 MHz Oscillator frequency can be changed by user request. Contact Trenz Electronic for details. clock signal used as a main system clock in FPGA designs. See the table below for pin-out information.

Signal	Frequency	FPGA pin
24MHZ1	24 MHz	G3
SYSCLK	100 MHz	AA12

Clock signals pin-out