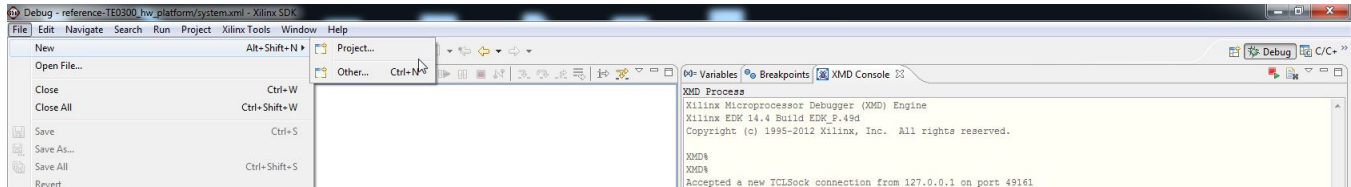


# Create an "Hardware Platform Specification Project" "reference-TE0300\_hw\_platform"

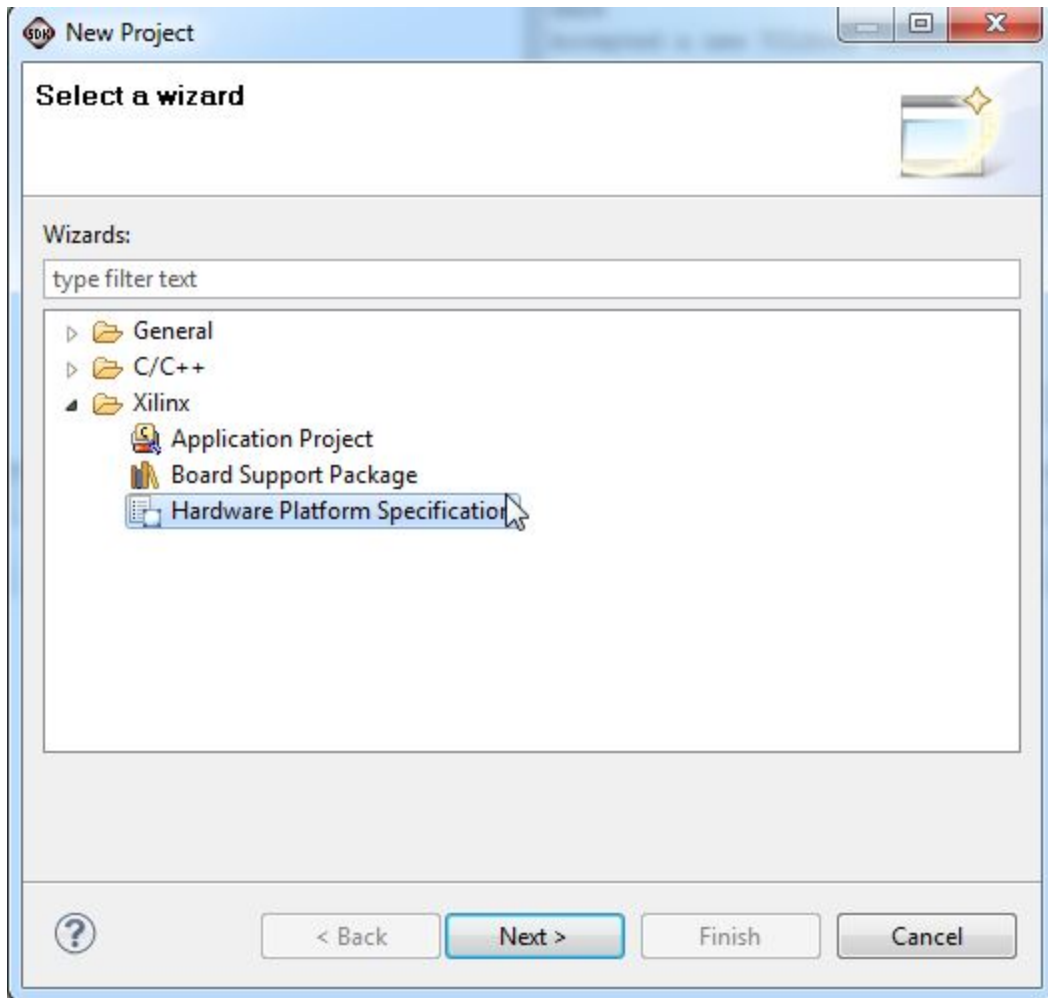
You should click "File" > "New" > "Project"



## Xilinx SDK New Project

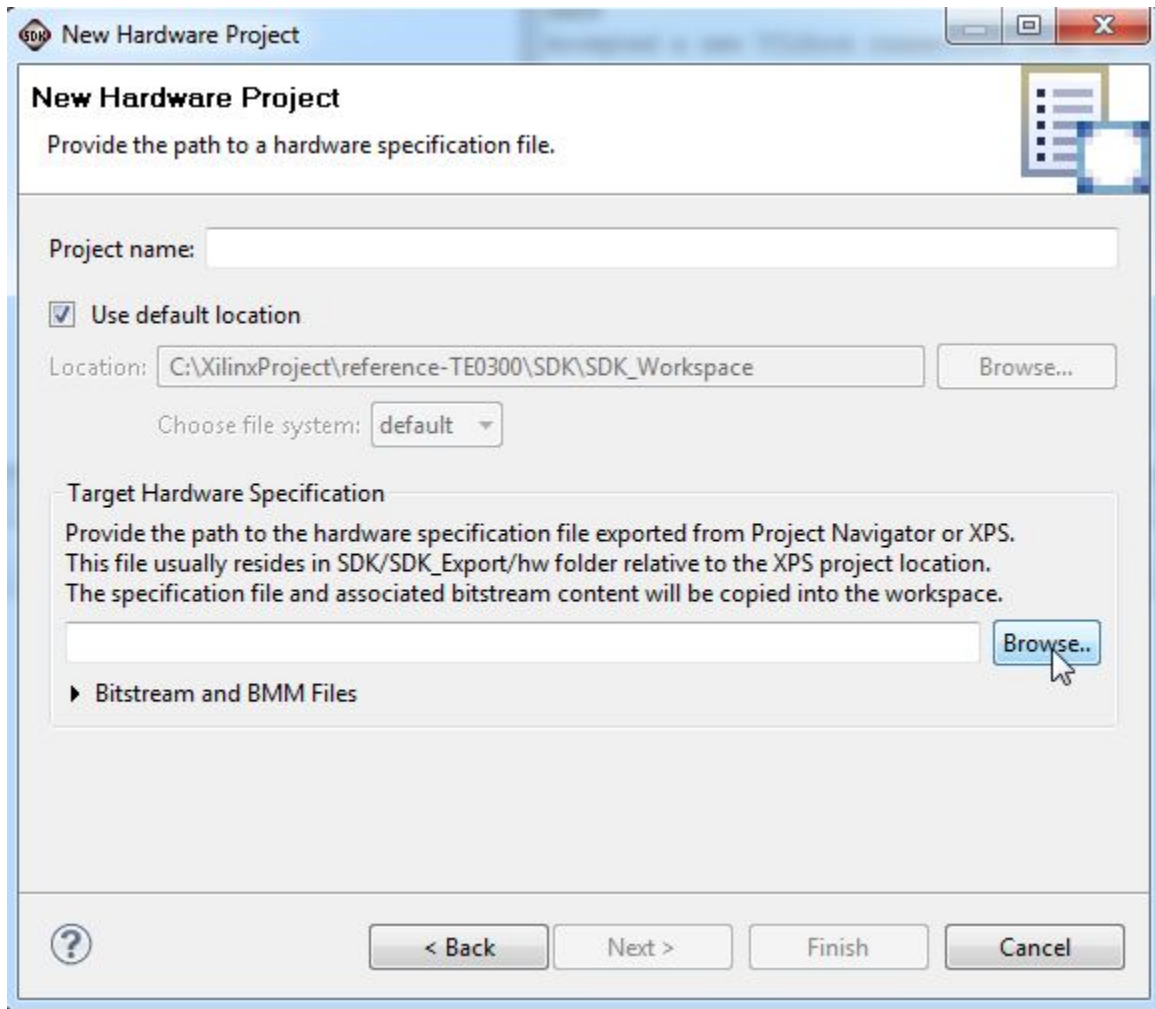
A pop up "New Project" will appear.

Click "Xilinx">"Hardware Platform Specification", then click "Next".



## Xilinx SDK new HW Platform Specification

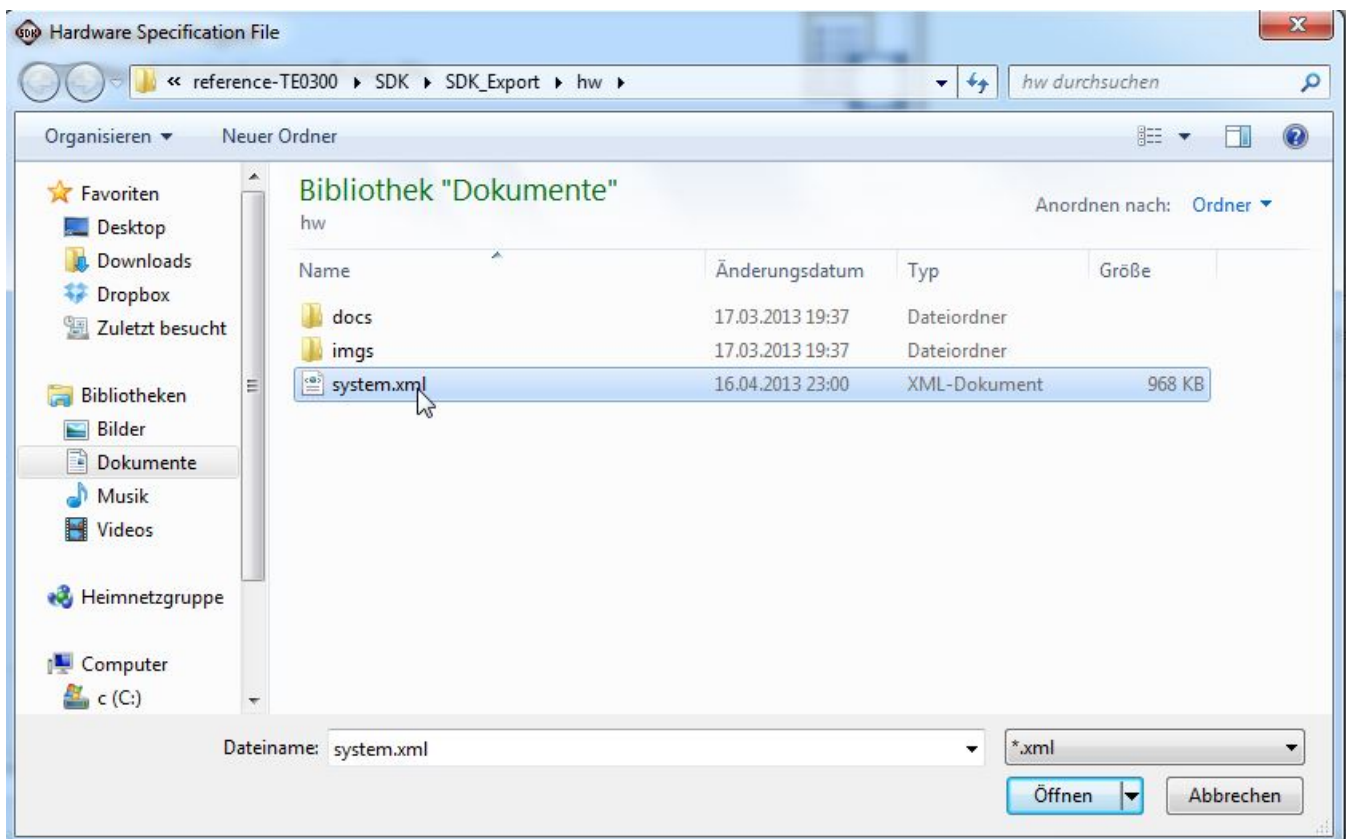
A new pop up "New Hardware Project" will appear



Xilinx SDK HW Project New Wizard 1

Under "Target Hardware Specification" click "Browse..." button.

A new pop up "Hardware Specification File" will appear.



#### "Hardware Specification File" pop-up

Select "system.xml", "C:\XilinxProject\reference-TE0300\SDK\SDK\_Export\hw\system.xml"

After the selection a new hardware specification (with name "reference-TE0300\_hw\_platform") appears in the Project Explorer of SDK.

C:/C++ - reference-TE0300\_hw\_platform/system.xml - Xilinx SDK

File Edit Source Refactor Navigate Search Run Project Xilinx Tools Window Help

Project Explorer

reference-TE0300\_hw\_platform

system.xml

### reference-TE0300\_hw\_platform Hardware Platform Specification

Design Information

Target FPGA Device: xc3s1600e  
Created With: EDK 14.4  
Created On: Tue Feb 26 14:04:05 2013  
XPS Design Report: [file:///C:/Xilinx/Project/reference-TE0300/SDK/SDK\\_Export/hw/system.html](file:///C:/Xilinx/Project/reference-TE0300/SDK/SDK_Export/hw/system.html)

#### Address Map for processor microblaze\_0

|                 |            |            |
|-----------------|------------|------------|
| dlmb_cntlr      | 0x00000000 | 0x00007fff |
| ilmb_cntlr      | 0x00000000 | 0x00007fff |
| led             | 0x81400000 | 0x8140ffff |
| spi_flash       | 0x83400000 | 0x8340ffff |
| debug_module    | 0x84400000 | 0x8440ffff |
| xps_intc_0      | 0x81800000 | 0x8180ffff |
| xps_timer_0     | 0x83c00000 | 0x83c0ffff |
| rs232           | 0x84000000 | 0x8400ffff |
| xps_i2c_slave_0 | 0xc6e00000 | 0xc6e0ffff |
| xps_fw2_0       | 0xc7200000 | 0xc720ffff |
| xps_npi_dma_0   | 0xc1a00000 | 0xc1a0ffff |
| ddr_sdram       | 0x1c000000 | 0x1fffffff |

#### IP blocks present in the design

|                   |                    |        |                           |
|-------------------|--------------------|--------|---------------------------|
| microblaze_0      | microblaze         | 7.30.b | <a href="#">Datasheet</a> |
| mb_plb            | plb_v46            | 1.05.a |                           |
| ilmb              | lmb_v10            | 1.00.a | <a href="#">Datasheet</a> |
| dlmb              | lmb_v10            | 1.00.a | <a href="#">Datasheet</a> |
| dlmb_cntlr        | lmb_bram_if_cntlr  | 2.10.b | <a href="#">Datasheet</a> |
| ilmb_cntlr        | lmb_bram_if_cntlr  | 2.10.b | <a href="#">Datasheet</a> |
| lmb_bram          | bram_block         | 1.00.a |                           |
| reset_logic       | util_reduced_logic | 1.00.a | <a href="#">Datasheet</a> |
| led               | xps_gpio           | 2.00.a |                           |
| spi_flash         | xps_spi            | 2.02.b | <a href="#">Datasheet</a> |
| ddr_sdram         | mpmc               | 6.06.a |                           |
| clock_generator_0 | clock_generator    | 4.03.a |                           |
| 4ahine_module     | mdm                | 1.00.a | <a href="#">Datasheet</a> |

Overview Source

Problems Tasks Console Properties Terminal

No consoles to display at this time.

Out Ma

An outline is not available.

Reference HW platform