

Switches: settings for USB Firmware, FPGA bitstream programming, Power Supply and reset.

Organized by modules

TE0300 features the [following slide switches](#):

- **S1:** 1 x slide switch (system)
- **S2:** 1 x slide switch (system)
- **S3:** 1 x slide switch (system)
- **S4:** 1 x slide switch (system)

TE0320 features the [following slide switches](#):

- **S1:** 4 x DIP slide switches (system)
- **S2:** 1 x slide switch (system)
- **S5:** 8 x DIP slide switches (user)

TE0630 features the [following slide switches](#):

- **S1:** 4 x DIP slide switches (system/user)
 - **S1[A]:** 1 x DIP slide switches (system)
 - **S1[B]:** 1 x DIP slide switches (system)
 - **S1[C:D]:** 2 x DIP slide switches (system)

Organized by types

EEPROM switch (aka FX2 Firmware Configuration selection)

EEPROM enabled: a connection between EEPROM and USB FX2 microcontroller exists.

EEPROM disabled: a connection between EEPROM and USB FX2 microcontroller doesn't exist.

EEPROM switch:

- TE0300: **S1**
- TE0320: **S1A**
- TE0630: **S1A**

 For all the switches above EEPROM is enabled when the switch is ON and the EEPROM is disabled when the switch is OFF.

 EEPROM switch is ON by default, to allow the USB FX2 microcontroller to read the [serial EEPROM](#) and enumerate as a [custom/specific USB device](#).

- When EEPROM switch is ON, the USB FX2 microcontroller can (re)write the [\(old\) serial EEPROM](#) to, for example, store a (new) [custom-specific firmware](#).
- When EEPROM switch is OFF, the USB FX2 microcontroller cannot read the serial EEPROM and enumerates as a [Cypress USB generic driver](#). It is used for [boot recovery](#).

Power Rails Configuration

Power Rails Configuration:

- TE0300: [S3](#)
- TE0320: [S2](#)
- TE0630: [S1B](#)

Master Reset

Master Reset (aka Reset Line):

- TE0300: [S2](#)
- TE0320: [S1D](#)
- TE0630: it doesn't exist as a switch but only as a button.

FPGA Configuration selection

FPGA Configuration selection (Mode Pins):

- TE0300: two mode selection pin are hardwired (they do not exist as switches), only one exist as a switch: [S4](#) (SPI+Jtag or only JTAG).
- TE0320: [S1\[B:C\]](#), more information [here](#).
- TE0630: all mode selection pins are hardwired (they do not exist as switches).