

Virtual COM Port Interface

Overview

Virtual COM port interface provide simple and convenient way to communicate with user FPGA project and manage on-board Flash and EEPROM memories. This application note can be used with Trenc Electronic modules with Cypress FX2 microcontroller and FPGA (TE0300, TE0320 and TE0630). You can find the code [vcom-cli](#), [vcom-async](#), [vcom-TE0300](#), [vcom-TE0320](#), [vcom-TE0320-async](#), [vcom-TE0630](#). Projects use example virtual COM port driver from Cypress [AN58764](#).

For this project you should use the CyUsbCOM driver. See [here](#).

Microcontroller firmware can operate in several modes:

- Transit data to and from FPGA
- Command Line Interface
- EEPROM write
- SPI Flash write