Set the repositories for device driver of custom block used in SDK Microblaze project

Click Xilinx "Xilinx Tools" and then "Repositories"



XilinxSDKProjectRepositories

After this a pop-up "Preferences" will appear.

e filter text	Add, remove or change the order of SDK's software repositories.	⇔ + ⇔ +
General	Local Repositories (available to the current workspace)	
Help		New
Install/Update		
Remote Systems Run/Debug		Kemove
Team		Up
Terminal		Down
Xilinx SDK		Relative
Flash Programming		
Hardware Specification	Global Repositories (available across workspaces)	
Log Information Level		New
Repositories Target Manager		Remove
XMD Startup		
		op
		Down
	SDK Installation Repositories	
	C:\Xilinx\14.4\ISE_DS\EDK\sw\lib\ C:\Xilinx\14.4\ISE_DS\EDK\sw\XilinxProcessorIPLib\ C:\Xilinx\14.4\ISE_DS\EDK\sw\ThirdParty\	
	L	
	Rescan Repositories	
	Note: Local repository settings take precedence over global repository settings.	
	Restore Defaults Apply	
4 11		2



Click "New..." button of "Local Repositories (available to the current workspace)"

Select "C:\XilinxProject\TE-EDK-IP".

Preferences		_ D X		
type filter text	Add, remove or change the order of SDK's software repositories.	⇔ • ⇔ • •		
General	Local Repositories (available to the current workspace)			
C/C++ Help	C:\XilinxProject\TE-EDK-IP	New		
Install/Update		Remove		
Remote Systems Run/Debug				
Team				
Xilinx SDK		Down		
Boot Image		Relative		
Hash Programming Hardware Specification	Hash Programming Hardware Specification Global Repositories (available across workspaces)			
Log Information Level		New		
Target Manager		Remove		
XMD Startup		Up		
		Down		
	SDK Installation Repositories			
	C:\Xilinx\14.4\ISE_DS\EDK\sw\lib\			
	C:\Xilinx\14.4\ISE_DS\EDK\sw\XilinxProcessorIPLib\ C:\Xilinx\14.4\ISE_DS\EDK\sw\ThirdParty\			
	Rescan Repositories			
	Note: Local repository settings take precedence over global repository settings.			
	······································			
	Restore Defaults Apply]		
		2		
•	ОК	Cancel		

Add reference with double indirection (as required by Xilinx SDK)

Click "Apply".

You should click "OK".

After this, you should wait until the building procedure ended.