## Generate a link script

You should click/select "demo" folder in "Project Explorer".

C/C++ - reference-TE0300_hw_platform/syst	em.xml - Xilinx SDK	
File Edit Source Refactor Navigate Sea	irch Run Project Xilinx Tools Window Help	
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🎦 Project Explorer 🛿 📃 🗖	🕼 system.xml 🛿 🙀 system.mss 🔒 interrupts.h 🔒 main.c 🍡	' 🗋
🖻 😫 🔻 🗸	reference-TE0300_hw_platform Hardware Platform Specification	-
reference-TE0300_hw_platform	Design Information	
um standaione_bsp_0	Target FPGA Device: xc3s1600e Created With: EDK 13.3 Created On: Thu Nov 22 15:40:10 2012	Ш
	Address Map for processor microblaze_0	
	dlmb_cntlr 0x00000000 0x00007fff	
	ilmb_cntlr 0x00000000 0x00007fff	
	SPI_FLASH_UX834UUUUU_UX834Uffff	
	debug_module 0x84400000 0x84401111	
	$xps_inc_0 0x83c00000 0x83c0ffff$	
	R\$232 0x84000000 0x8400fff	
	xps j2c slave 0 0xc6e00000 0xc6e0fff	
	xps fx2 0 0xc7200000 0xc720ffff	
	xps_npi_dma_0 0xc1a00000 0xc1a0ffff	
	DDR_SDRAM 0x1c000000 0x1ffffff	
		Ŧ
	Overview Source	

Click/select "demo" folder in "Project Explorer". Case A) Update the SDK Workspace: the Hardware reference is 'old' (13.3 in this image)



You should click "Xilinx Tools" and then click "Generate linker script".



Generate linker script for application project "demo"

A pop-up "Generate a linker script" will appear; you should click "Generate";

Control your application's memory r	nap.					ľ
Output Settings Project: demo Output Script: C:\XilinxProject\reference-TE0300\ Modify project build settings as folk Set generated script on all project b Hardware Memory Map Memory ilmb_cntlr_dlmb_cntlr ddr_sdram_MPMC_BASEADDR	SDK\SDK_Workspac	e\demo\src\lscript.ld	Browse	Basic Advanced Place Code Sections in: Place Data Sections in: Place Heap and Stack in: Heap Size: Stack Size:	ilmb_cntir_dlmb_cntir ilmb_cntir_dlmb_cntir ilmb_cntir_dlmb_cntir 1 KB 1 KB	

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The pop-up will close and "lscript.ld" file will be (re)written in "src" subfolder of "demo".

After this, you should wait that building (compile and linking) procedure ends; "Finished Building: demo.elf.elfcheck" should appear in Console C-build [demo].