

# POWER command

This command controls some FPGA power supply sources.

Byte	Value	Description
1	0xAF	POWER command ID
2	power	0 = Power OFF state, 1 = Power ON state
From 3 to 64	-	Not used

## POWER Command Packet Layout.

Byte	Description
1	0 = Power OFF state, 1 = Power ON state
From 2 to 64	Not used

## POWER Reply Packet Layout.