Download the bitstream and the elf file

Now you should click "Xilinx Tools" and select "Program FPGA"



Now you should select the .bit bitstream file (that create the Microblaze system) and the .elf file (that runs on the Microblaze system) to download in Microblaze BRAM memory; in this case select bootloop. The next step will automatically download demo.elf on Microblaze.

Program FP	GA	
Specify the bit	stream and the ELF files that reside in BRAM memory	G H
Hardware Con	figuration	_
Hardware Spec	ification: C:\XilinxProject\reference-TE0300\SDK\SDK_Workspace\reference-TE0300_hw_platform\system	.xml
Bitstream: C:	\XilinxProject\reference-TE0300\SDK\SDK_Workspace\reference-TE0300_hw_platform\system.bit	Browse.
BMM File: C:	\XilinxProject\reference-TE0300\SDK\SDK_Workspace\reference-TE0300_hw_platform\system_bd.bmm	Browse.
Software Conf	guration	
Processor	ELF File to Initialize in Block RAM	
microblaze_0	C:\XilinxProject\reference-TE0300\SDK\SDK_Worksp; +	
	bootloop C\XilinyProject\reference_TE0300\SDK\SDK_Workspace\demo\Debug\demo.effs	
	Browse.	

Program FPGA step 2, select .bit file bitstream and the .elf file running on Microblaze