

TE0818 Reference Designs

Description for the newest Design Version

Reference Designs	Demo Designs	Tutorial Designs
• TE0818 Test Board ◦ Vitis/Vivado 2023.2 ◦ QSPI ◦ Custom Carrier (minimum PS Design with available module components only) ◦ Modified FSBL (some additional outputs only)	• --	• --
• TE0818 StarterKit ◦ Vitis/Vivado 2023.2 ◦ TEBF0818 ◦ Linux ◦ USB ◦ ETH ◦ MAC from EEPROM ◦ PCIe ◦ SATA ◦ SD ◦ I2C ◦ RGPIO ◦ Display Port (DP) ◦ user LED access ◦ Modified FSBL for Si5345 programming		

Basic Documentation and Notes

- [AMD Development Tools](#)
 - [TE Board Part Files](#)
 - [TE Board Part Installation Options](#)
- [PetaLinux KICKstart](#)
- [TE Reference Design - Project Delivery](#)

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