TEI0022 Reference Designs

Description for the newest Design Version

- TEI0022 Test Board
  - Quartus Prime Lite 21.1
  - Intel SoC FPGA EDS Standard Edition 20.1
  - Yocto
  - SD
  - UART
  - ETH
  - USB
  - I2C
  - QSPI
  - HDMI
  - MAC from EEPROM
  - DDR3 memory
  - User LED

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