

TE0802 Resources

MPSoC development board with Xilinx Zynq UltraScale+ FPGA

- [TE0802](#) - Wiki with TRM, reference projects, application notes and more
 - [TE0802 TRM](#) - Technical Reference Manual
 - [TE0802 Reference Designs](#) - Reference Design description for Vivado 2017.2 and newer
 - [TE0802 Getting Started Guide](#) - Getting Started with TE0802
 - [TE0802 PCN](#) - Product Change Notification (see also Schematic Change History and TRM)
 - [TE0802 AN](#) - Advisory Note

Links

- [Shop TE0802](#) - contains prices, available stock, disposability, scope of delivery and more
- [Download area](#) - various reference designs, schematics, hardware designs and more
- [Pinout/Tracelength Table](#) - illustration of pinout, tracelength and cross reference Information of modules with different baseboards

3rd Party Links

- [Xilinx Vitis AI facedetect and resnet50 Demo on Trenz Electronic TE0802 02 with ZU2CG and 1 GB LPDD4](#) - provided by UTIA

Support

For support, please go to <http://forum.trenz-electronic.de/> or contact support@trenz-electronic.de