

TE0817 Reference Designs

Description for the newest Design Version

Reference Designs	Demo Designs	Tutorial Designs
<ul style="list-style-type: none">• TE0817 Test Board<ul style="list-style-type: none">◦ Vitis/Vivado 2022.2◦ QSPI◦ Custom Carrier (minimum PS Design with available module components only)◦ Modified FSBL (some additional outputs only)• TE0817 StarterKit<ul style="list-style-type: none">◦ Vitis/Vivado 2022.2◦ TEBF0818◦ Linux◦ USB◦ ETH◦ MAC from EEPROM◦ PCIe◦ SATA◦ SD◦ I2C◦ GPIO◦ Display Port (DP)◦ user LED access◦ Modified FSBL for Si5345 programming	• --	• --

Basic Documentation and Notes

- [Xilinx Development Tools](#)
 - [TE Board Part Files](#)
 - [TE Board Part Installation Options](#)
- [Petalinux KICKstart](#)
- [TE Reference Design - Project Delivery](#)

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