

PCN-20230322 TE0722-02 to TE0722-04 Hardware Revision Change

[Download PDF version of this document.](#)

Company	Trenz Electronic GmbH
PCN Number	PCN-20230322
Title	TE0722-02 to TE0722-04 Hardware Revision Change
Subject	Hardware Revision Change
Issue Date	2023-06-27

Products Affected

This change affects all Trenz Electronic TE0722 SoMs: TE0722-02*.

Affected Product	Replacement
TE0722-02	TE0722-04-41C-4-A
TE0722-02I	TE0722-04-41I-4-A
TE0722-02-07S-1C	TE0722-04-11C-4-A

Changes

#1 Changed DCDC EN5311QI (U2, U3) to MPM3834CGPA and adapted power circuit.

Type: Schematic Change

Reason: EOL of Component.

Impact: None. Increased current output capability. Minor changes in electrical characteristics.

#2 Changed ferrid bead BKP0603HS121-T (L1, L2, L4, L5) to MPZ0603S121HT000.

Type: Schematic Change

Reason: EOL of Component.

Impact: None.

#3 Set proximity/ambient light sensor Si1143-A11-GMR (U4) and circuit components (D1, D3, D5, R6, and R20) to not fitted.

Type: Schematic Change

Reason: EOL of Component.

Impact: Sensor is no longer available. Custom design needs to be updated by customer.

#4 Added resistors (R37, R38, R39) assembly option to prepare proximity /ambient light sensor (U4) circuit for Si115x usage.

Type: Schematic Change

Reason: EOL of Si1143-A11-GMR.

Impact: Si115x could be assembled.

#5 Changed MEMS oscillator (U8) from SiT8008AI-73-XXS-33.333333E to SiT8008BI-73-XXS-33.333333E.

Type: BOM change

Reason: EOL of Component.

Impact: None. Clock revision changed.

#6 Added jumper (J4) and resistor (R40) to enable JTAG only boot mode and changed resistor (R11) from 19.1 kOhm to 10 kOhm.

Type: Schematic Change

Reason: QSPI programming problems with newer Vivado versions.

Impact: None. Fix QSPI programming problems with newer Vivado versions according to [AR#00002 - QSPI Programming issues](#).

#7 Renamed signal name from "SPI-DQ0/M0" to "SPI-DQ0/M3".

Type: Schematic Change

Reason: Use AMD boot mode name convention.

Impact: None.

#8 Renamed signal name from "SPI-DQ3/M3" to "SPI-DQ3/M0".

Type: Schematic Change

Reason: Use AMD boot mode name convention.

Impact: None.

#9 Modified testpoints : Added (TP4, TP5) / Changed size from 1 mm to 0.8 mm diameter (TP1, TP2, TP3, TP7, TP8).

Type: PCB Change

Reason: Use standard testpoints.

Impact: None.

#10 Modified fiducials: Added (PM5, PM6) / Changed (PM1, PM2, PM3, PM4).

Type: PCB Change

Reason: Use standard fiducial setting.

Impact: None.

#11 Added CE and UKCA logo.

Type: PCB Change

Reason: Required logos.

Impact: None.

#12 Added legal notices, revision history, block and power diagram. Updated page count and order.

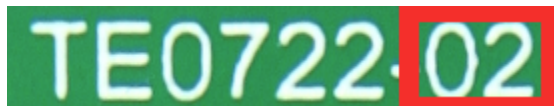
Type: Documentation Update

Reason: Documentation improvement.

Impact: None.

Method of Identification

The revision number is printed on the top side of the PCB.



Production Shipment Schedule

From January 2024, after old stock is gone. If the new revision is not suitable for your application and still the former revision of the board is needed, please contact us.

Contact Information

If you have any questions related to this PCN, please contact Trenz Electronics Technical Support at

- forum.trenz-electronic.de
- wiki.trenz-electronic.de
- support@trenz-electronic.de (subject = PCN-20230322)
- phone
 - national calls: 05741 3200-0
 - international calls: 0049 5741 3200-0

Disclaimer

Any projected dates in this PCN are based on the most current product information at the time this PCN is being issued, but they may change due to unforeseen circumstances. For the latest schedule and any other information, please contact your local Trenz Electronic sales office, technical support or local distributor.

This PCN follows JEDEC Standard J-STD-046.