PCN-20230601 TEF1002-02 Q4 rise time change

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Company	Trenz Electronic GmbH
PCN Number	PCN-20230601
Title	TEF1002-02 Q4 rise time change
Subject	Q4 rise time change
Issue Date	2023-06-01

Products Affected

This change affects all Trenz Electronic TEF1002-02 boards with serial numbers greater than 754352.

Affected Products	
TEF1002-02	

Changes

#1 Changed R95 from 00hm to 2.2kOhm and C13 from 10nF to 100nF

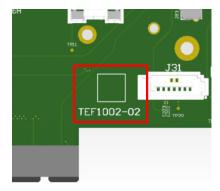
Type: BOM change

Reason: 3V3_PER rail can be disabled/enabled via attached FPGA/SoC module using a RGPIO IP core to communicate with CPLD on TEF1002. If this function is used, enabling can lead to a reset of the module. This is due to a large inrush current, which is disturbing the module power supply. To prevent this the rise time is enlarged.

Impact: With new resistor and capacitor values the calculated 10%-90% rise time of Q4 is 0.4ms.

Method of Identification

The serial number is written on a white QR code sticker, placed on the top side of the PCB. The model code and revision number (TEF1002-02) are printed on the top side of the PCB.



Production Shipment Schedule

With immediate effect the new variant is shipped. If the new revision is not suitable for your application and still the former revision of the board is needed, please contact us.

Contact Information

If you have any questions related to this PCN, please contact Trenz Electronics Technical Support at

- forum.trenz-electronic.de
- wiki.trenz-electronic.de
- support%trenz-electronic.de (subject = PCN-20230601)
- phone
 - o national calls: 05741 3200-0
 - o international calls: 0049 5741 3200-0

Disclaimer

Any projected dates in this PCN are based on the most current product information at the time this PCN is being issued, but they may change due to unforeseen circumstances. For the latest schedule and any other information, please contact your local Trenz Electronic sales office, technical support or local distributor.

This PCN follows JEDEC Standard J-STD-046.