TEG2000 TRM

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Overview 1 Overview

The Trenz Election: The Ecology is a FPGA module integrating a CologneChip GateMate FPGA, a QSPI Flash, level shifter LFDS and Several clocking and power components necessary for all on-board voltages. Numerous antipute that a credit card size at a very competitive price. All Trenz Electronic SoMs in 4 x 5 cm form factor are mechanically compatible.

Refer to http://terip.org/tedp/uol-info for the current online version of this manual and other available documentation. 3.1 QSPI Flash

• 3.2 Oscillator

• 4 Configuration and System Control Signals

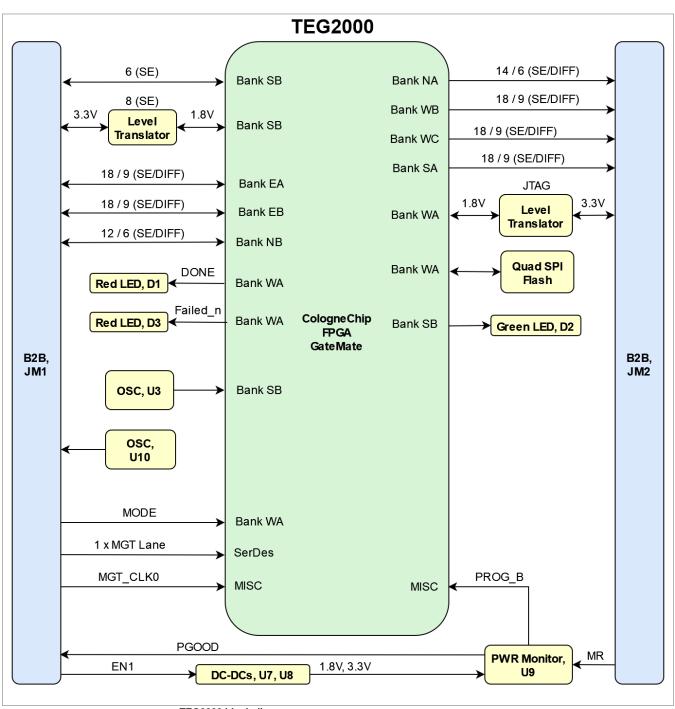
- - 5.1 Power Rails
- SoC/FFGAModuleended Power up Sequencing
- 6 Board to Board to Board freehip Gate Mate A1 / A2 / A4 1)
- Bould & CologheChip GateMate A1 / A2 / A4 ''

 6.1 Connector Mating height
 6.2 Connector Mating height
 6.4 Connector Speed recommy, speed 1)
 6.4 Connector Speed recommy, speed 1)
 6.5 Connector Mechanical Ratings
 6.6 Matthe Connector Mechanical Ratings
 6.7 Operations
 6 Conscillator, Neuriprum Ratings (Second Second Seco

- S Uterface Offered Variants
 S Revisión AixtB2B Connector (LSHM)
 S Configuration via Flash on board, USB or JTAG
- 9. 25 Gb/s Sr Desiger Agents
 10 Disclamar B2B Connector (LSHM)

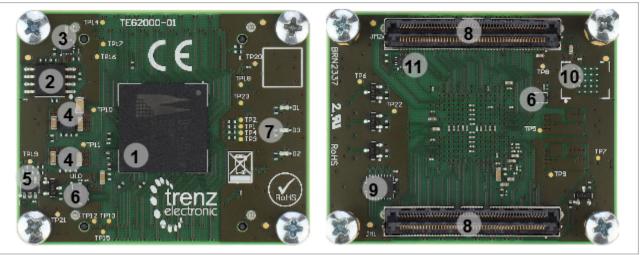
 - 10.1 Data P40/30 single ended IO / up to 57 differential pairs
 10.2 Document@Varranty
 - Power 10.3 Limitation of Liability
 - 9034306 powert supply via B2B Connector needed
 - Dimension Technology Licenses
 - ° °104€ ≝r50rmmental Protection
 - Notes 10.7 REACH, RoHS and WEEE
- 111 Please, gake care of the possible assembly options.
 - ²⁾ Up to 16 MByte are possible.

Block Diagram



TEG2000 block diagram

Main Components



TEG2000 main components

- 1. CologneChip GateMate FPGA, U1

- Colognechip Gatek
 QSPI Flash, U5
 Power Switch, Q1
 DCDC, U7, U8
 Power Monitor, U9
 Power Monitor, U9
- 6. Oscillator, U10, U3
 7. LEDs, D1, D2, D3

- B2B connector, JM2, JM1
 Level shifter, U2
 Optional additional Flash, U4
 BUS-Transceiver, U6

Initial Delivery State

Storage device name	Content	Notes			
Quad SPI Flash	Blinky Demo Design	U5			
Initial delivery state of programmable devises on the module					

Initial delivery state of programmable devices on the module

Signals, Interfaces and Pins

Connectors

Connector Type	Designator	Interface	IO CNT	Notes
B2B	JM1	SerDes	8	
B2B	JM1	GPIO	48	Bank NB/EB/EA, powered by VCCIOA
B2B	JM1	GPIO	6	up to 3.3V due to level shifter, connected to Bank SB

B2B	JM1	GPIO	6	up to 1.8V, also connected to Bank SB
B2B	JM1	configuration Signals	3	EN1, PGOOD , MODE
B2B	JM2	GPIO	50	Bank NA/WB /WC, powered by VCCIOD
B2B	JM2	GPIO	18	Bank SA, powered by VCCIOC
B2B	JM2	JTAG	4	03.3VIN
B2B	JM2	MR	1	low active Reset
B2B	JM1	CLK	DIFF CLK	

Board Connectors

Test Points

Test Point	Signal	Side	Notes ¹⁾
TP1	NA_6_N TP1	top	FPGA IO
TP2	NA_6_P	top	FPGA IO
ТРЗ	NB_B3	top	FPGA IO
TP4	NB_A3	top	FPGA IO
TP5	1V	bottom	1V power rail
TP6	DONE	bottom	FPGA CFG_DONE pin
TP7	PROG_B	bottom	FPGA RST_N pin
TP8	GND	bottom	
TP9	GND	bottom	
TP10	1V	top	1V power rail
TP11	1.8V	top	OUT, 1.8V power rail
TP12	VIN	top	VIN (3.3 - 5.0V)
TP13	3.3VIN	top	
TP14	3.3V	top	OUT, 3.3V power rail
TP15	VCCIOA	top	IN 1.1V 2.7V, powers IO Banks NB/EB/EA
TP16	VCCIOC	top	IN 1.1V 2.7V, powers IO Bank SA
TP17	VCCIOD	top	IN 1.1V 2.7V, powers IO Banks NA/WB/WC
TP18	DONE	top	FPGA CFG_DONE pin
TP19	PROG_B	top	FPGA RST_N pin
TP20	GND	top	
TP21	GND	top	
TP22	FAILED_n	bottom	FPGA CFG_FAILED pin

		TP23	FAILED_n	top	FPGA CFG_FAILED pin
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¹⁾ Direction:

• IN: Input from the point of view of this board.

• OUT: Output from the point of view of this board.

Test Points Information

On-board Peripherals

Chip/Interface	Designator	Connected To	Notes
QSPI Flash	U5	FPGA Bank CFG WA	SPIx4 Interface for FPGA configuration
Oscillator	U3	FPGA Bank SB, Pin IO_SB_A8	25 MHz
Oscillator	U10	JM1.16, JM1.18	differential 100 MHz Clock for Gigabit-Transceiver

On board peripherals

Configuration and System Control Signals

Connector+Pin	Signal Name	Direction ¹⁾	Description
JM1.28	EN1	IN	activates DC-DCs
JM1.30	PGOOD	OUT	Output from power monitor
JM1.32	MODE	IN	configuration mode - 0 JTAG or 1 SPI active Mode
JM2.18	MR	IN	low active Reset connected to the Power Monitor that triggers PROG_B (FPGA RST_N)
JM2.93 / JM2.95 / JM2.97 / JM2.99	TMS / TDI / TDO / TCK	Signal-dependent	JTAG configuration and debugging interface. JTAG reference voltage: 3.3V

¹⁾ Direction:

IN: Input from the point of view of this board.
 OUT: Output from the point of view of this board.

Power and Power-On Sequence

Power Rails

Power Rail Name/ Schematic Name	Connector + Pin	Direction ¹⁾	Notes
VIN	JM1.1 / JM1.3 / JM1.5 / JM2.2 / JM2.4 / JM2.6 / JM1.8	IN	3.3 V - 5.0V, Micromodule Power
3.3VIN	JM1.13 / JM1.15	IN	Micromodule Power
VCCIOA	JM1.9 / JM1.11	IN	
VCCIOC	JM2.5	IN	
VCCIOD	JM2.7 / JM2.9	IN	
3.3V	JM2.10 / JM2.12 / JM2.91	OUT	Power for Carrier, powers on module the level shifter, LEDs and control Pins
1.8V	JM1.39	OUT	Power for Carrier, On module it powers the Flash, FPGA VDD and Banks SB,WA
1V			Powers the FPGA core, PLLs and SerDes interfac

¹⁾ Direction:

IN: Input from the point of view of this board.
OUT: Output from the point of view of this board.

Module power rails.

Recommended Power up Sequencing

Sequence	Net namieecor	nmended Voltage	Ra i®guel -up/down	Description	Notes
0	-	-	-	Configuration signal setup.	See Configuratio n and System Control Signals.
1	VIN	3.3 V - 5.0 V (± 5 %)	-	Main power supply	
1	EN1	-	PU ¹⁾ , 3.3VIN	power enable	
1	PGOOD	-	PU ¹⁾ , 3.3V	power good status.	
2	3.3VIN	3.3 V (± 5 %)	-	Main power supply	Main module power supply. 0.5 A minimum. Power consumption depends mainly on design and cooling solution.
3	VCCIOA, VCCIOC, VCCIOD	1.1 V - 2.7 V	-	Bank Voltages	1.8 V on TE0703 Carrier

¹⁾ (on module)

Baseboard Design Hints

Board to Board Connectors

These connectors are hermaphroditic. Odd pin numbers on the module are connected to even pin numbers on the baseboard and vice versa.

4 x 5 modules use two or three Samtec Razor Beam LSHM connectors on the bottom side.

- 2 x REF-189016-02 (compatible to LSHM-150-04.0-L-DV-A-S-K-TR), (100 pins, "50" per row)
 1 x REF-189017-02 (compatible to LSHM-130-04.0-L-DV-A-S-K-TR), (60 pins, "30" per row) (depending on module)

Connector Mating height

0

When using the same type on baseboard, the mating height is 8mm. Other mating heights are possible by using connectors with a different height

Order number	Connector on baseboard	compatible to	Mating height
23836	REF-189016-01	LSHM-150-02.5-L-DV-A- S-K-TR	6.5 mm
	LSHM-150-03.0-L-DV-A- S-K-TR	LSHM-150-03.0-L-DV-A- S-K-TR	7.0 mm
23838	REF-189016-02	LSHM-150-04.0-L-DV-A- S-K-TR	8.0 mm
	LSHM-150-06.0-L-DV-A- S-K-TR	LSHM-150-06.0-L-DV-A- S-K-TR	10.0mm
26125	REF-189017-01	LSHM-130-02.5-L-DV-A- S-K-TR	6.5 mm
	LSHM-130-03.0-L-DV-A- S-K-TR	LSHM-130-03.0-L-DV-A- S-K-TR	7.0 mm
24903	REF-189017-02	LSHM-130-04.0-L-DV-A- S-K-TR	8.0 mm
	LSHM-130-06.0-L-DV-A- S-K-TR	LSHM-130-06.0-L-DV-A- S-K-TR	10.0mm

Connectors.

The module can be manufactured using other connectors upon request.

Connector Speed Ratings

The LSHM connector speed rating depends on the stacking height; please see the following table:

Stacking height	Speed rating
12 mm, Single-Ended	7.5 GHz / 15 Gbps
12 mm, Differential	6.5 GHz / 13 Gbps
5 mm, Single-Ended	11.5 GHz / 23 Gbps
5 mm, Differential	7.0 GHz / 14 Gbps

Speed rating.

Current Rating

Current rating of Samtec Razor Beam[™] LSHM B2B connectors is 2.0A per pin (2 adjacent pins powered).

Connector Mechanical Ratings

- Shock: 100G, 6 ms Sine
- Vibration: 7.5G random, 2 hours per axis, 3 axes total

Manufacturer Documentation

Modified
07 04, 2016 by Tho rsten Trenz

Download All

Technical Specifications

Absolute Maximum Ratings *)

Power Rail Name/ Schematic Name	Description	Min	Мах	Unit
VIN	Micromodule Power	-0.3	6.5	V
3.3VIN	Micromodule Power	-0.3	6.5	V
VCCIOA	Bank NB/EB/EA voltage	-	2.75	V
VCCIOC	Bank SA voltage	-	2.75	V
VCCIOD	Bank NA/WB/WC voltage	-	2.75	V

Absolute maximum ratings

*) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Condition. Exposure to absolute-maximum rated conditions for extended periods may affect device reliability.

Recommended Operating Conditions

This TRM is generic for all variants. Temperature range can be differ depending on the assembly version. Voltage range is mostly the same during variants (exceptions are possible, depending on custom request)

Operating temperature range depends also on customer design and cooling solution. Please contact us for options.

- Variants of modules are described here: Article Number Information
- Modules with commercial temperature grade are equipped with components that cover at least the range of 0°C to 75°C
- Modules with extended temperature grade are equipped with components that cover at least the range of 0°C to 85°C
- Modules with industrial temperature grade are equipped with components that cover at least the range of -40°C to 85°C
- The actual operating temperature range will depend on the FPGA / SoC design / usage and cooling and other variables.

Parameter	Min	Мах	Units	Reference Document
VIN	3,135	5,25	V	
3.3VIN	3,135	3,465	V	
VCCIOA	1.1	2.75	V	
VCCIOC	1.1	2.75	V	
VCCIOD	1.1	2.75	V	

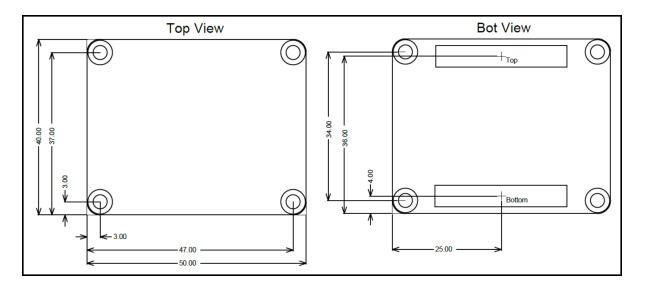
Recommended operating conditions.

Physical Dimensions

- Module size: 40 mm × 50 mm. Please download the assembly diagram for exact numbers.
- Mating height with standard connectors: 8 mm.

PCB thickness: 1.4 mm.

All dimensions are shown in millimeters.



Physical Dimension

Currently Offered Variants

Trenz shop TEG2000 overview page			
English page	German page		
Trenz Electronic Shop Overview			

Revision History

Hardware Revision History



Board hardware revision number.

Date	Revision	Changes	Documentation Link
-	REV01	First Production Release	REV01
Hardware Revision History			

Hardware revision number can be found on the PCB board together with the module model number separated by the dash.

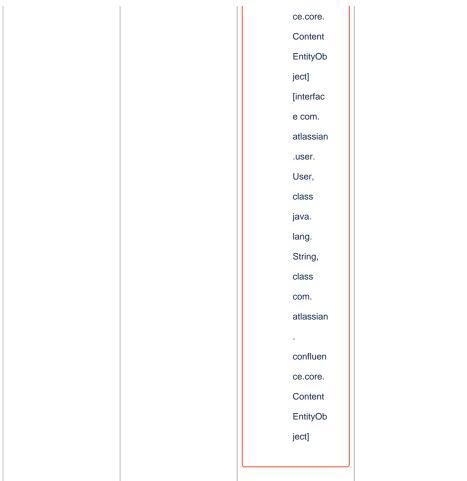
Document Change History

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REACH, RoHS and WEEE

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