TE0729 Resources

TE0729 - 5.2 x 7.6 cm SoC module with Xilinx Zynq-7020

- TE0729 - Wiki with TRM, reference projects, application notes and more
  - TE0729 TRM - Technical Reference Manual
  - TE0729 CPLD Firmware - Firmware Description
  - TE0729 Product Change Notifications - PCN (see also Schematic Change History and TRM)
  - TE0729 Reference Designs - Reference Design Description for Vivado 2017.2 and newer
- 5.2 x 7.6 SoM BTE and BSE B2B Connectors - overview, compatibility, mating heights and speed rating

Links

- Shop TE0729 - contains prices, available stock, disposability, scope of delivery and more
- Download area - various reference designs, schematics, hardware designs and more
- Pinout/Tracelength Table - illustration of pinout, tracelength and cross reference Information of modules with different baseboards

5.2 x 7.6 Carrier

- 5.2 x 7.6 Carriers - overview of available carrier boards for all 5.2 x 7.6 cm SoM modules

Notes

Watchdog does not work correctly on all modules with Firmware released before 2017.08.22. Please update Firmware on CPLD. For questions, write to Trenz Electronic support.

2 Firmware variants with swapped external reset input and output direction are available. See TE0729 Resources#Watchdog section of this document. Firmware for J2-89 as external reset output and J2-91 as external reset input will be used as default firmware.

Support

- For support, please go to http://forum.trenz-electronic.de/ or contact support@trenz-electronic.de