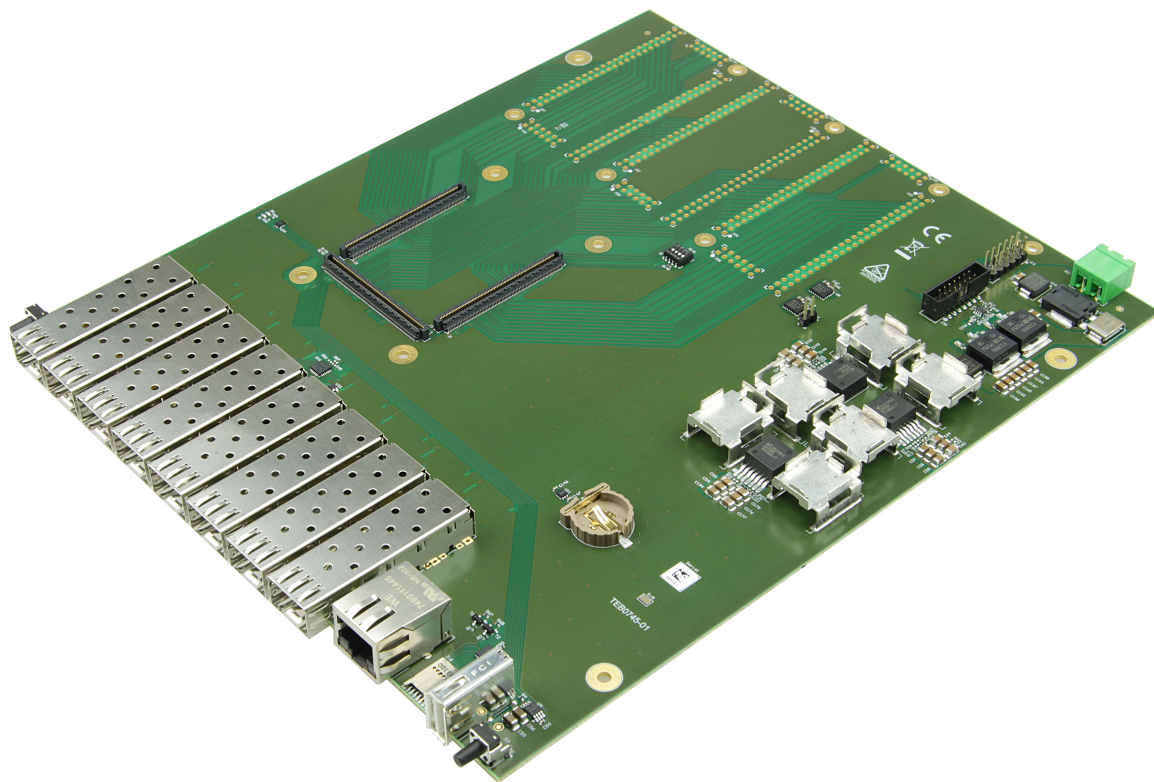
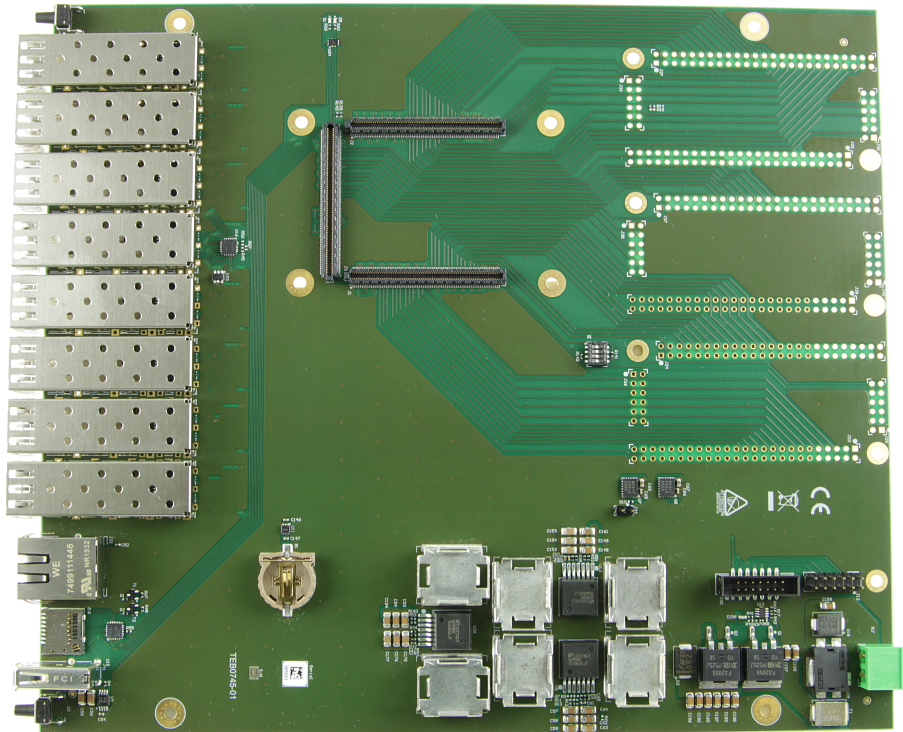


TEB0745 Getting Started





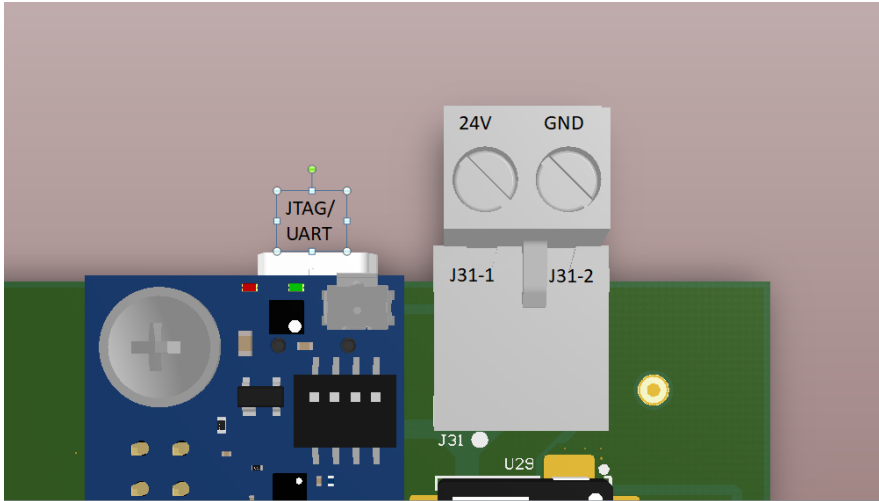
Key Features

- Form factor 20 cm x 23,1 cm
- Trenz TE0745 Module Socket (3 x Samtec connectors)
- 24V power supply over ARKZ950/2 connecting terminal
- 1 x EMI Network Filter
- 3 x Variable Step Down Regulator Module (VDRM) with head sink
- 2 x Synchronous Buck Regulator
- 1 x XMOD (TE0790) Pin Header (JTAG / UART)
- 1 x Pin Header (JTAG)
- 1 x microSD connector
- 1 x RJ45 Ethernet connector
- 1 x USB Host Connector
- 8 x SFP-Connector
- 6 x Pin Header 50 pol. (FPGA Bank I/O's and Power)
- 6 x Pin Header 12 pol. (FPGA Bank I/O's and Power)
- 1 x battery holder
- 2 x DIP Switch Array (VCC_HR_B / Modi)
- 2 x Button (User / Reset)
- 2 x LED (Green)

Power supply

24V on J31:

- J31-1: 24V
- J31-2: GND



TE0790(XMOD) SwitchSettings

DIP	Value
S2-1	ON
S2-2	OFF
S2-3	OFF
S2-4	ON

TEB0745 DIP Switch and Button Settings

S4 set HR-Bank Power.

S4-1	S4-2	S4-3	S4-4	Power VCC_HR_B
OFF	OFF	OFF	N.C.	1,8V
OFF	OFF	ON	N.C.	1,5V
OFF	ON	OFF	N.C.	3,3V
OFF	ON	ON	N.C.	2,5V
ON	OFF	OFF	N.C.	0,8V
ON	OFF	ON	N.C.	Do not use
ON	ON	OFF	N.C.	1,25V
ON	ON	ON	N.C.	1,2V

S1 for Modi and PS User switch.

	ON	OFF
S1-1	Boot Mode SD	Boot Mode Flash
S1-2	Module CPLD in Chain	Module FPGA in Chain
S1-3	N.C.	N.C.
S1-4	User Switch to MIO12	User Switch to MIO12

S2 Button: User Reset

S3 Button: User Button to MIO50