

TE0723

Overview

The Trenez Electronic TE0723 is an Arduino compatible FPGA module integrating a Xilinx Zynq-7010, 512 MByte DDR3L, and 16 MByte SPI Flash Memory for configuration and operation.

All parts are at least commercial temperature range of 0°C to +70°C. The module operating temperature range depends on customer design and cooling solution. Please contact us for options.



Only PCB Revision 01 is Open Source.

	REV 1	REV 2
DDR	LPDDR2	DDR3L
DDR Size	64MB	512MB (128MB)
PMOD	no	yes

Resources

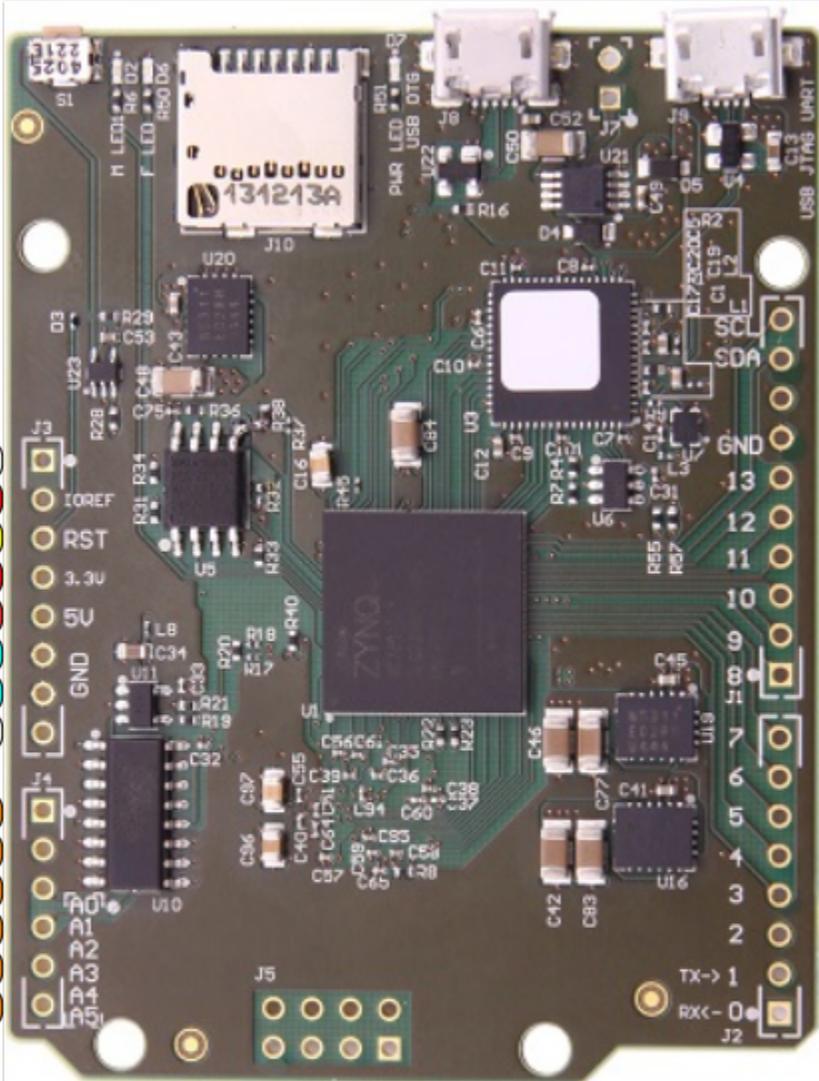
- [TE0723 Resources](#)

Key Features

- Xilinx XC7Z010-1CLG225C
- Form Factor: Arduino Shield
- Dual ARM Cortex-A9
- 512 MByte DDR3L
- 16 MByte SPI Flash memory
- 12 MHz MEMS Oscillator low power consumption
- Hi-Speed USB2.0 ULPI Transceiver
- 23 FPGA I/O's available on board-to-board connectors
- MicroSD Card socket
- Micro USB OTG
- RGB LED (PL I/O connected)
- "Done" LED (inverted polarity)
- On-board USB JTAG and UART
- CERN Open Hardware Licence 1.2

PCB Revision 1

FT232H USB UART/FIFO
Channel A JTAG to Zynq
Channel B UART or FIFO



- NC
- IOREF
- RST
- 3.3V
- 5V
- GND
- GND
- NC

- A0
- A1
- A2
- A3
- A4
- A5

- D15 P13
- D14 R13
- NC
- GND
- D13 R12
- D12 P11
- D11 R11
- P10 P10
- D9 R10
- D8 P9
- D7 N8
- D6 R8
- D5 P8
- D4 R7
- D3 N7
- D2 N12
- D1
- D0

- 3.3V
- RST
- 3.3V
- TXD

ESP8266 Interface
ESP-01 Pinout

RX
GPC
GP
GN