

TE0712 Resources

TE0712 - 4 x 5 cm FPGA module with Xilinx Artix-7

- [TE0712](#) - Wiki with TRM, reference projects, application notes and more
 - [TE0712 TRM](#) - Technical Reference Manual
 - [TE0712 CPLD Firmware](#) - firmware description
 - [TE0712 Reference Designs](#) - reference design description for Vivado 2017.2 and newer
 - [TE0712 Product Change Notifications](#) - PCN (see also schematic change history and TRM)
 - [TE0712 AN](#) - Design and Advisory Notes
- [4 x 5 SoM Integration Guide](#) - module and carrier board power connection, PCB design notes and more
- [4 x 5 SoM LSHM B2B Connectors](#) - overview, compatibility, mating heights and speed rating
- [4 x 5 SoMs Handling and Usage Precautions](#) - general handling and removal instructions

Links

- [Shop TE0712](#) - contains prices, order number, content ...
- [Download area](#) - various reference designs, schematics, hardware designs and more
- [Pinout/Tracelength Table](#) - illustration of pinout, tracelength and cross reference Information of modules with different baseboards

4 x 5 Carrier

- [4 x 5 SoM Carriers](#) - overview of available carrier boards for all 4 x 5 cm modules

Support

- For support, please go to <http://forum.trenz-electronic.de/> or contact support@trenz-electronic.de