

TE0808 Reference Designs

Description for the newest Design Version

Reference Designs	Demo Designs	Tutorial Designs
• TE0808 Test Board ◦ Vitis/Vivado 2023.2 ◦ QSPI ◦ Custom Carrier (minimum PS Design with available module components only) ◦ Modified FSBL (some additional outputs only)	• TE0808 SK Demo1 ◦ Vitis/Vivado 2019.2 ◦ petalinux ◦ Linux Debian 9 (Stretch) or Linux Ubuntu 18.04 (Bionic Beaver) ◦ DisplayPort ◦ TEBF0808 ◦ USB ◦ ETH (use EEPROM MAC) ◦ MAC from EEPROM ◦ PCIe ◦ SATA ◦ SD ◦ I2C ◦ RGPIO ◦ DP ◦ user LED access ◦ Modified FSBL for Si5338 programming ◦ Special FSBL for QSPI Programming	• TE0808 StarterKit Vitis AI 3.0 Tutorial Error rendering macro 'excerpt-include' User 'null' does not have permission to view the page.
• TE0808 StarterKit ◦ Vitis/Vivado 2023.2 ◦ TEBF0808 ◦ Linux ◦ USB ◦ ETH ◦ MAC from EEPROM ◦ PCIe ◦ SATA ◦ SD ◦ I2C ◦ RGPIO ◦ Display Port (DP) ◦ user LED access		
• TE0808 IBERT ◦ Vitis/Vivado 2019.2 ◦ TEBF0808 ◦ PL IBERT ◦ Modified FSBL for Si5338 programming ◦ Special FSBL for QSPI Programming		

Basic Documentation and Notes

- [AMD Development Tools](#)

- [TE Board Part Files](#)
 - [TE Board Part Installation Options](#)
- [PetaLinux KICKstart](#)
- [TE Reference Design - Project Delivery](#)

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