TE0720 Reference Designs

Description for the newest Design Version

- TE0720 Test Board
  - Vitis/Vivado 2023.2
  - PetaLinux
  - SD
  - ETH (use EEPROM MAC)
  - USB
  - I2C
  - RTC
  - VIO PHY LED
  - FSBL for EEPROM MAC and CPLD access / petalinux patch
- TE0720 HDMI701
  - Vitis/Vivado 2019.2
  - PetaLinux/Ubuntu/Debian
  - SD
  - ETH (use EEPROM MAC)
  - USB
  - I2C
  - RTC
  - VIO PHY LED
  - HDMI
  - FSBL for EEPROM MAC and CPLD access and HDMI DMA
  - Special FSBL for QSPI Programming
  - TE0701 (only supported)
- TE0720 ETH0706
  - Vitis/Vivado 2019.2
  - PetaLinux
  - SD
  - ETH (use EEPROM MAC for on board PHY)
  - **second ETH from TE0706 carrier**
  - USB
  - I2C
  - RTC
  - VIO PHY LED
  - FSBL for EEPROM MAC and CPLD access
  - Special FSBL for QSPI Programming
- TE0720 SDSoC EDDP FOC with TE0701
  - EDDP with FOC algorithm designed in SDSoC for TE0701 carrier board.
  - Automated generation of SDSoC platforms for family of TE0720 modules.
- TE0720 SDSoC EDDP FOC with TE0706
  - EDDP with FOC algorithm designed in SDSoC for TE0706 carrier board.
  - Automated generation of SDSoC platforms for family of TE0720 modules.
- TE0720 SDSoC
  - SDSoC platform export
  - SDSoC examples (baremetal and linux)

Basic Documentation and Notes

- AMD Development Tools
  - TE Board Part Files
  - TE Board Part Installation Options
- PetaLinux KICKstart
- TE Reference Design - Project Delivery

Download

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