TEI0004 TRM

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Overview

Arrow USB Programmer2 is an FT2232H based JTAG Adapter supported by Intel Quartus.

Key Features

- Supported by Intel Quartus (JTAG Mode only)
- Intel JTAG Compatible Pinout
- Additional UART Channel available
- Based on FTDI FT2232H USB2 Interface
- Micro USB Connector
- RED activity LED
- GREEN Power-on LED

Block Diagram

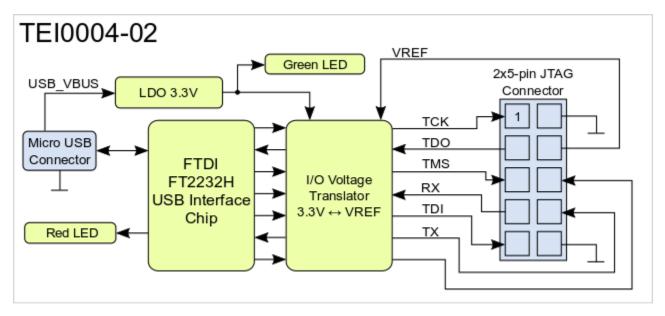


Figure 1: TEI0004-02 Block Diagram.

Main Components





Figure 2: TEI0004-02 main components.

- FTDI FT2232H IC
 RED LED (Activity)
 Green LED (Power-on)
- 4. Micro USB2 Connector
- **5.** 2x5-pin JTAG Connector (White dot marks Pin 1)

Signals, Interfaces and Pins

JTAG Connector Pinout

The 2x5 female socket have to be connected to the corresponding pin header on the target system. The signal assignment of the pin header on the adapter board is fully compatible to original USB blaster. Furthermore there is also an UART interface available and I/O-pin reserved for future use.

Following table describes the pin-assignment to the signals of the interfaces:

Signal	Pin Number	Pin Number	Signal
TCK (output from adapter)	1	2	GND
TDO (input to adapter)	3	4	Reference I/O-voltage from target board for JTAG and UART
TMS (output from adapter)	5	6	Reserved Output (May be used as Processor Reset in future software releases)
UART RX (input to adapter)	7	8	UART TX (output from adapter)
TDI (output from adapter)	9	10	GND

Table 1: JTAG Connector pin assignment.

USB Interface

The USB interface is provided by the FTDI FT2232H IC. The entire USB protocol is handled on chip and compatible to USB 2.0 High Speed (480 MBps) and Full Speed (12 MBps).

On-board Peripherals

FTDI FT2232H IC

FTDI FT2232H IC Channel A is used in MPPSE Mode for JTAG, Channel B is available as UART. FT2232H EEPROM is programmed with Arrow Programmer2 Identificator to be recognized by the support library for Quartus.

On-board LEDs

On-board LEDs indicating power-on and JTAG activity:

Color	Description
Green	Power-on LED
Red	JTAG activity

Table 2: On-board LEDs.

Power

Power supply of the adapter board

Arrow Programmer2 is powered via USB.

Technical Specifications

Absolute Maximum Ratings

Parameter	Min	Max	Units	Reference Document
VREF	-0.5	4.6	V	Nexperia 74AVCH4T245 data sheet
USB VBUS	4.75	5.25	V	USB 2.0 Specification
Voltage on I/O pins	-0.5	4.6	V	Nexperia 74AVCH4T245 data sheet
Storage temperature	-40	+90	°C	LED 19-213/R6C-AL1M2VY/3T data sheet

Table 3: Absolute maximum ratings.

Recommended Operating Conditions

Parameter	Min	Max	Units	Reference Document
VREF	0.8	3.6	٧	Nexperia 74AVCH4T245 data sheet
USB VBUS	4.75	5.25	٧	USB 2.0 Specification
Voltage on I/O pins	0	VREF	٧	Nexperia 74AVCH4T245 data sheet
Operating temperature	-40	+85	°C	FTDI FT2232H data sheet

Table 4: Recommended operating conditions.

Operating Temperature Range

Industrial grade: -40°C to +85°C.

Arrow Programmer2 can be used within industrial temperature range.

Physical Dimensions

- Module size: 14.2mm x 20.8mm. Please download the assembly diagram for exact numbers.
 Highest part on PCB: 7.37 mm. Please download the step model for exact numbers.

All dimensions are given in millimeters and mil.

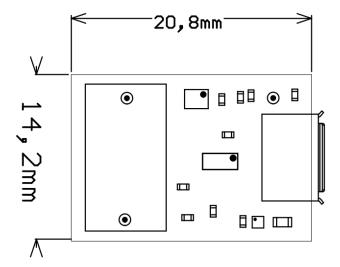


Figure 3: Physical dimensions drawing.

Revision History

Hardware Revision History

Date	Revision	Notes	PCN	Documentation Link
-	01	Prototypes	-	-
-	02	First production release.	-	TEI0004

Table 5: Hardware revision history.

Hardware revision number can be found on the PCB board together with the module model number separated by the dash.



Figure 4: Revision number.

Document Change History

Date Revision	Contributors	Description
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Ambiguous method overloading for method jdk. proxy279.\$Proxy4022#hasCon tentLevelPermission. Cannot resolve which method to invoke for [null, class java. lang.String, class com. atlassian.confluence.pages. Page] due to overlapping prototypes between: [interface com.atlassian.confluence.user. ConfluenceUser, class java. lang.String, class com. atlassian.confluence.core. ContentEntityObject] [interface com.atlassian.user.User, class java.lang.String, class com.atlassian.confluence.core. ContentEntityObject]

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P Bugfix VREF recomme nded voltage range

2022-05-05	v.50	John Hartfiel	• renaming
2017-11-23	v.31	Ali Naseri	updated block diagram
2017-11-21	v.25	Ali Naseri	• First TRM release

Table 6: Document change history.

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Data Privacy

Please also note our data protection declaration at https://www.trenz-electronic.de/en/Data-protection-Privacy

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02 Sept 2017