

# TEI0001 Resources

## MAX1000 2.5 x 6.15 cm FPGA module with Intel MAX10 FPGA

- [TEI0001 - MAX1000](#) - Wiki with TRM, reference projects, application notes and more
  - [TEI0001 TRM](#) - Technical Reference Manual
  - [TEI0001 Getting Started](#) - basic information
  - [TEI0001 Reference Designs](#) - Reference Design Description for Quartus 17.1 and newer
  - [TEI0001 PCN](#) - Product Change Notification (see also Schematic Change History and TRM)
- [Arrow USB Programmer](#) - Arrow USB Programmer 2 driver setup installation instruction and Troubleshoot

## Links

- [Shop TEI0001](#) - contains prices, available stock, disposability, scope of delivery and more
- [Download area](#) - various schematics, hardware designs and more
- [Arrow USB Programmer Setup Libraries](#) - driver
- [Pinout/Tracelength Table](#) - illustration of pinout, tracelength and cross reference Information of modules with different baseboards

## Forum

- [MAX1000 community projects](#)
  - [DEMO MAX1000: USER GUIDE .... first steps with MAX1000](#) - User Guide
  - [MAX1000 Hardware](#) - Hardware Notes
  - [MAX1000 Driver and Setup Files](#) - Driver Notes
  - [MAX1000 Demo Projects](#) - Shipped LED DEMO Design

## 3rd Party Links

- [Effective Verilog learning with Intel FPGA partially demonstrated on the MAX1000](#) - provided by Muhammad Tahir Rana
- [Litex FPGA build system support](#) - provided by LiteX-Hub community

## Support

- For support, please go to [Trenz Electronic Forum](#) or contact [support@trenz-electronic.de](mailto:support@trenz-electronic.de).