TE0725 Reference Designs

Description for the newest Design Version

- TE0725 Test Board
  - Vivado/Vitis 2021.2
  - MicroBlaze
  - QSPI
  - I2C
  - UART
- TE0725 HyperRAM
  - Vivado/Vitis 2021.2
  - MicroBlaze
  - QSPI
  - I2C
  - UART
  - HyperRAM
  - S/Labs HBMC IP (Free Trail IP)

Basic Documentation and Notes

- AMD Development Tools
  - TE Board Part Files
  - TE Board Part Installation Options
- PetaLinux KICKstart
- TE Reference Design - Project Delivery

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