

TE0841 Resources

TE0841 - 4 x 5 cm FPGA module with Xilinx Kintex UltraScale

- [TE0841](#) - Wiki with TRM, reference projects, application notes and more
 - [TE0841 TRM](#) - Technical Reference Manual
 - [TE0841 Reference Designs](#) - Reference Design Description for Vivado 2017.2 and newer
 - [TE0841 CPLD Firmware](#) - Firmware Description
 - [TE0841 AN](#) - Design and Advisory Notes
- [4 x 5 SoM Integration Guide](#) - module and carrier board power connection, PCB Design notes and more
- [4 x 5 SoM LSHM B2B Connectors](#) - overview, compatibility, mating heights and speed rating
- [4 x 5 SoMs Handling and Usage Precautions](#) - general handling and removal instructions

Links

- [Shop TE0841](#) - contains prices, available stock, disposability, scope of delivery and more
- [Download area](#) - various reference designs, schematics, hardware designs and more
- [Pinout/Tracelength Table](#) - illustration of pinout, tracelength and cross reference Information of modules with different baseboards

4 x 5 Carrier

- [4 x 5 SoM Carriers](#) - overview of available carrier boards for all 4 x 5 cm modules

Support

- For support, please go to <http://forum.trenz-electronic.de/> or contact support@trenz-electronic.de