

TE0725LP Resources

TE0725LP - 3.5 x 7.3 cm FPGA module with Xilinx Artix-7

- [TE0725LP](#) - Wiki with TRM, reference projects, application notes and more
 - [TE0725LP TRM](#) - Technical Reference Manual
 - [TE0725LP Reference Designs](#) - Reference Design Description for Vivado 2017.2 and newer
- [3.5 x 7.3 SoM differences](#) - Differences between TE0725 and TE0725LP variants

Links

- [Shop TE0725LP](#) - contains prices, available stock, disposability, scope of delivery and more
- [Download area](#) - various reference designs, schematics, hardware designs and more
- [TE0790 Resources](#) - XMOD JTAG Programmer documentation
- [Pinout/Tracelength Table](#) - illustration of pinout, tracelength and cross reference Information of modules with different baseboards

Support

- For support, please go to <http://forum.trenz-electronic.de/> or contact support@trenz-electronic.de