

TE0722 Reference Designs

Description for the newest Design Version

- [TE0722 Test Board](#)
 - Vivado 2023.2
 - UART
 - I2C
 - SD
 - Modified FSBL for DDR Less Zynq + small app with LED+Sensor and SD Card access
 - Special FSBL for QSPI programming

Basic Documentation and Notes

- [AMD Development Tools](#)
 - [TE Board Part Files](#)
 - [TE Board Part Installation Options](#)
- [PetaLinux KICKstart](#)
- [TE Reference Design - Project Delivery](#)

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Notes



Xilinx Zynq devices in CLG225 package do not support SD Card boot directly from ROM bootloader.



TE0722 is without DDR, so special FSBL (sources on reference designs) is needed, see also: [DDR less ZYNQ Design](#)