

TE0729 Reference Designs

Description for the newest Design Version

Reference Designs	Demo Designs	Tutorial Designs
• TE0729 Test Board ◦ Vitis/Vivado 2019.2 ◦ PetaLinux ◦ SD ◦ ETH (1 x 1 GBit, 2 x 100 MBit) ◦ USB ◦ I2C ◦ RTC ◦ Watchdog Test example over VIO ◦ Modified FSBL to select optional eMMC instead of SD ◦ Special FSBL for QSPI programming	• --	• --

Basic Documentation and Notes

- [AMD Development Tools](#)
 - [TE Board Part Files](#)
 - [TE Board Part Installation Options](#)
- [PetaLinux KICKstart](#)
- [TE Reference Design - Project Delivery](#)

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