

TEI0003 Reference Designs

Description for the newest Design Version

- [TEI0003 Test Board](#)
 - Quartus Prime Lite 22.1std
 - Nios V/m
 - SPI
 - UART
 - SDRAM controller (AXI4)
 - Source code: https://github.com/ultraembedded/core_sdram_axi4
 - 3-axis Accelerometer
 - User LEDs
 - User buttons

Basic Documentation and Notes

- [Intel Development Tools](#)
- [Project Delivery - Intel devices](#)

Download

- [TEI0003 Reference Designs](#)