## **TE0783 CPLD**

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### Overview

Firmware for PCB CPLD with designator U32. Second CPLD Device in Chain: LCMX02-4000HC

# **Feature Summary**

- Power Management
- Boot Mode
- Reset
- UART
- IO Expender(RGPIO)

# Firmware Revision and supported PCB Revision

See Document Change History

# **Product Specification**

## **Port Description**

Name / opt. VHD Name	Direction	Pin	Bank Power	Description
BM2/MIO4 / BM2_MIO4	out	B35	3.3V	Boot Mode Pin to FPGA (SD or QSPI)

CONNIGOS         Inc.         BSS         SSS         UNITARY           CPLD_GPIDIO         1         83         3.3V         B2B Lucremity_not_used           CPLD_GPIDIO         1         81         3.3V         B2B currenty_not_used           CPLD_GPIDIO         1         81         3.3V         B2B currenty_not_used           CPLD_GPIDIO         1         4         3.3V         B2B currenty_not_used           CPLD_GPIDIO         in         A5         3.3V         B2B currenty_not_used           CPLD_GPIDIO         in         A5         3.3V         B2B currenty_not_used           ENT_JOT         inout         A3         EXT_JO_VCC         B2B, RGPID           EXT_JOT         inout         A24         EXT_JO_VCC         B2B, RGPID           EXT_JOT         inout         A24         EXT_JO_VCC         B2B, RGPID           EXT_JOTA         inout         A28         EXT_JO_VCC         B2B, RGPID           EXT_JOTA         inout         A28         EXT_JO_VCC         B2B, RGPID           EXT_JOTA         inout         A29         EXT_JO_VCC         B2B, RGPID           EXT_JOTA         inout         A22         EXT_JO_VCC         B2B, RGPID	BOOTMODE	out	B32	3.3V	B2B UART from MIO15
CPLD_GPIO0         Inc.         A3         3.3V         B2B / currently, not_used           CPLD_GPIO1         Inc.         B1         3.3V         B2B / currently, not_used           CPLD_GPIO2         Inc.         A1         3.3V         B2B / currently, not_used           CPLD_GPIO3         Inc.         A2         3.3V         B2B / currently, not_used           DONE         Inc.         A35         3.3V         PPGA Dose signal           ENT_IV         out         A35         3.3V         disable/mable module power 1V and all other related voltages           EXT_IO10         inout         A24         EXT_IO_VCC         B2B, RGPIO           EXT_IO11         inout         A24         EXT_IO_VCC         B2B, RGPIO           EXT_IO12         inout         A23         EXT_IO_VCC         B2B, RGPIO           EXT_IO13         inout         B28         EXT_IO_VCC         B2B, RGPIO           EXT_IO14         inout         A28         EXT_IO_VCC         B2B, RGPIO           EXT_IO15         inout         B18         EXT_IO_VCC         B2B, RGPIO           EXT_IO19         inout         A9         EXT_IO_VCC         B2B, RGPIO           EXT_IO21         inout         A9 <td< td=""><td></td><td></td><td></td><td></td><td></td></td<>					
CPLD_GPIO1         B1         3.3V         B2B / currently, not_used           CPLD_GPIO2         1         A1         3.3V         B2B / currently, not_used           CPLD_GPIO3         In         A2         3.3V         B2B / currently, not_used           CPLD_GPIO3         In         A2         3.3V         PFGA Done signal           EN_IV         out         B3         3.3V         disable/creable module power IV and all other related voltages           EXT_JO11         inout         A33         EXT_JO_VCC         B2B, RGPIO           EXT_JO11         inout         A24         EXT_JO_VCC         B2B, RGPIO           EXT_JO12         inout         A24         EXT_JO_VCC         B2B, RGPIO           EXT_JO13         inout         B21         EXT_JO_VCC         B2B, RGPIO           EXT_JO14         inout         A28         EXT_JO_VCC         B2B, RGPIO           EXT_JO15         inout         A28         EXT_JO_VCC         B2B, RGPIO           EXT_JO16         inout         A2         EXT_JO_VCC         B2B, RGPIO           EXT_JO17         inout         A2         EXT_JO_VCC         B2B, RGPIO           EXT_JO19         inout         A20         EXT_JO_VCC         B2B,					
CPLD_GPIO2         In         A1         3.3V         B2B / currently, not_used           CPLD_GPIO3         in         A2         3.3V         B2B, used for Boot Mode           DONE         in         A35         3.3V         B2B, used for Boot Mode           ENT_IV         oul         B3         3.3V         disable/renable module power 1V and all other related voltages           EXT_JO1         inout         A33         EXT_JO_VCC         B2B, RGPIO           EXT_JO10         inout         A24         EXT_JO_VCC         B2B, RGPIO           EXT_JO12         inout         A24         EXT_JO_VCC         B2B, RGPIO           EXT_JO13         inout         A24         EXT_JO_VCC         B2B, RGPIO           EXT_JO14         inout         A28         EXT_JO_VCC         B2B, RGPIO           EXT_JO16         inout         A28         EXT_JO_VCC         B2B, RGPIO           EXT_JO17         inout         A22         EXT_JO_VCC         B2B, RGPIO           EXT_JO17         inout         A22         EXT_JO_VCC         B2B, RGPIO           EXT_JO19         inout         A29         EXT_JO_VCC         B2B, RGPIO           EXT_JO20         inout         A20         EXT_JO_VCC					·
CPLD_GPIO3         in         A2         3.3V         B2B, used for Boot Mode           DOME         in         A35         3.3V         FPGA Dome signal           EN_1V         out         B3         3.3V         FPGA Dome signal           ENT_IO1         inout         A33         EXT_IO_VCC         B2B, RGPID /           EXT_IO11         inout         A24         EXT_IO_VCC         B2B, RGPID           EXT_IO12         inout         A23         EXT_IO_VCC         B2B, RGPID           EXT_IO13         inout         B21         EXT_IO_VCC         B2B, RGPID           EXT_IO14         inout         A28         EXT_IO_VCC         B2B, RGPID           EXT_IO15         inout         A28         EXT_IO_VCC         B2B, RGPID           EXT_IO16         inout         A22         EXT_IO_VCC         B2B, RGPID           EXT_IO16         inout         A9         EXT_IO_VCC         B2B, RGPID           EXT_IO18         inout         A9         EXT_IO_VCC         B2B, RGPID           EXT_IO19         inout         A9         EXT_IO_VCC         B2B, RGPID           EXT_IO22         inout         B4         EXT_IO_VCC         B2B, RGPID           EXT_					·
DONE         in         A35         3.3V         FPGA Done signal           EN_IV         out         B3         3.3V         disable/enable module power IV and all other related voltages           EXT_JO1         inout         A33         EXT_JO_VCC         B2B, RGPIO           EXT_JO11         inout         A24         EXT_JO_VCC         B2B, RGPIO           EXT_JO12         inout         A23         EXT_JO_VCC         B2B, RGPIO           EXT_JO13         inout         B21         EXT_JO_VCC         B2B, RGPIO           EXT_JO14         inout         A28         EXT_JO_VCC         B2B, RGPIO           EXT_JO15         inout         A28         EXT_JO_VCC         B2B, RGPIO           EXT_JO16         inout         A28         EXT_JO_VCC         B2B, RGPIO           EXT_JO17         inout         A22         EXT_JO_VCC         B2B, RGPIO           EXT_JO18         inout         A9         EXT_JO_VCC         B2B, RGPIO           EXT_JO19         inout         A9         EXT_JO_VCC         B2B, RGPIO           EXT_JO20         inout         A9         EXT_JO_VCC         B2B, RGPIO           EXT_JO21         inout         A9         EXT_JO_VCC         B2B, RGPIO <td></td> <td>in</td> <td></td> <td></td> <td>·</td>		in			·
ENT_IV         Out         B3         3.3V         disable/enable module power 1V and all other related voltages           EXT_IO1         Inout         A33         EXT_IO_VCC         B28, RGPIO /           EXT_IO10         Inout         B22         EXT_IO_VCC         B28, RGPIO           EXT_IO12         Inout         A24         EXT_IO_VCC         B28, RGPIO           EXT_IO13         Inout         B21         EXT_IO_VCC         B28, RGPIO           EXT_IO14         Inout         A28         EXT_IO_VCC         B28, RGPIO           EXT_IO15         Inout         B18         EXT_IO_VCC         B28, RGPIO           EXT_IO16         Inout         A22         EXT_IO_VCC         B28, RGPIO           EXT_IO17         Inout         B8         EXT_IO_VCC         B28, RGPIO           EXT_IO18         Inout         A9         EXT_IO_VCC         B28, RGPIO           EXT_IO19         Inout         A9         EXT_IO_VCC         B28, RGPIO           EXT_IO21         Inout         B14         EXT_IO_VCC         B28, RGPIO           EXT_IO22         Inout         B14         EXT_IO_VCC         B28, RGPIO           EXT_IO23         Inout         A18         EXT_IO_VCC         B2					
EXT_JO10         inout         A33         EXT_JO_VCC         B2B, RGPIO           EXT_JO11         inout         A24         EXT_JO_VCC         B2B, RGPIO           EXT_JO11         inout         A24         EXT_JO_VCC         B2B, RGPIO           EXT_JO12         inout         A23         EXT_JO_VCC         B2B, RGPIO           EXT_JO13         inout         B21         EXT_JO_VCC         B2B, RGPIO           EXT_JO14         inout         A28         EXT_JO_VCC         B2B, RGPIO           EXT_JO16         inout         A22         EXT_JO_VCC         B2B, RGPIO           EXT_JO18         inout         A22         EXT_JO_VCC         B2B, RGPIO           EXT_JO18         inout         A2         EXT_JO_VCC         B2B, RGPIO           EXT_JO19         inout         A2         EXT_JO_VCC         B2B, RGPIO           EXT_JO20         inout         B4         EXT_JO_VCC         B2B, RGPIO           EXT_JO22         inout         B4         EXT_JO_VCC         B2B, RGPIO           EXT_JO23         inout         B7         EXT_JO_VCC         B2B, RGPIO           EXT_JO24         inout         A18         EXT_JO_VCC         B2B, RGPIO <t< td=""><td></td><td></td><td></td><td></td><td></td></t<>					
EXT_JO10					
EXT_IO11					
EXT_JO12	_				
EXT_IO13					
EXT_IO14         inout         A28         EXT_IO_VCC         B28, RGPIO           EXT_IO15         inout         B18         EXT_IO_VCC         B28, RGPIO           EXT_IO16         inout         A22         EXT_IO_VCC         B28, RGPIO           EXT_IO17         inout         A9         EXT_IO_VCC         B28, RGPIO           EXT_IO18         inout         A9         EXT_IO_VCC         B28, RGPIO           EXT_IO2         inout         A20         EXT_IO_VCC         B28, RGPIO           EXT_IO20         inout         B24         EXT_IO_VCC         B28, RGPIO           EXT_IO20         inout         B44         EXT_IO_VCC         B28, RGPIO           EXT_IO21         inout         A8         EXT_IO_VCC         B28, RGPIO           EXT_IO22         inout         B7         EXT_IO_VCC         B28, RGPIO           EXT_IO23         inout         A18         EXT_IO_VCC         B28, RGPIO           EXT_IO24         inout         A18         EXT_IO_VCC         B28, RGPIO           EXT_IO25         inout         A5         EXT_IO_VCC         B28, RGPIO           EXT_IO26         inout         A19         EXT_IO_VCC         B28, RGPIO <td< td=""><td></td><td></td><td></td><td></td><td></td></td<>					
EXT_IO15					
EXT_IO16         inout         A22         EXT_IO_VCC         B2B, RGPIO           EXT_IO17         inout         B8         EXT_IO_VCC         B2B, RGPIO           EXT_IO18         inout         A9         EXT_IO_VCC         B2B, RGPIO           EXT_IO19         inout         A20         EXT_IO_VCC         B2B, RGPIO           EXT_IO20         inout         B14         EXT_IO_VCC         B2B, RGPIO           EXT_IO21         inout         A8         EXT_IO_VCC         B2B, RGPIO           EXT_IO22         inout         B7         EXT_IO_VCC         B2B, RGPIO           EXT_IO23         inout         B13         EXT_IO_VCC         B2B, RGPIO           EXT_IO24         inout         A18         EXT_IO_VCC         B2B, RGPIO           EXT_IO25         inout         A5         EXT_IO_VCC         B2B, RGPIO           EXT_IO26         inout         A1         EXT_IO_VCC         B2B, RGPIO           EXT_IO27         inout         A1         EXT_IO_VCC         B2B, RGPIO           EXT_IO28         inout         A1         EXT_IO_VCC         B2B, RGPIO           EXT_IO30         inout         A6         EXT_IO_VCC         B2B, RGPIO           EX					
EXT_IO17         Inout         B8         EXT_IO_VCC         B2B, RGPIO           EXT_IO18         Inout         A9         EXT_IO_VCC         B2B, RGPIO           EXT_IO19         Inout         A20         EXT_IO_VCC         B2B, RGPIO           EXT_IO2         Inout         B24         EXT_IO_VCC         B2B, RGPIO           EXT_IO20         Inout         B14         EXT_IO_VCC         B2B, RGPIO           EXT_IO21         Inout         A8         EXT_IO_VCC         B2B, RGPIO           EXT_IO22         Inout         B7         EXT_IO_VCC         B2B, RGPIO           EXT_IO23         Inout         B13         EXT_IO_VCC         B2B, RGPIO           EXT_IO24         Inout         A18         EXT_IO_VCC         B2B, RGPIO           EXT_IO25         Inout         A5         EXT_IO_VCC         B2B, RGPIO           EXT_IO26         Inout         A13         EXT_IO_VCC         B2B, RGPIO           EXT_IO27         Inout         A13         EXT_IO_VCC         B2B, RGPIO           EXT_IO28         Inout         A6         EXT_IO_VCC         B2B, RGPIO           EXT_IO30         Inout         A6         EXT_IO_VCC         B2B, RGPIO           E					
EXT_IO18         inout         A9         EXT_IO_VCC         B2B, RGPIO           EXT_IO19         inout         A20         EXT_IO_VCC         B2B, RGPIO           EXT_IO2         inout         B24         EXT_IO_VCC         B2B, RGPIO           EXT_IO20         inout         B14         EXT_IO_VCC         B2B, RGPIO           EXT_IO21         inout         A8         EXT_IO_VCC         B2B, RGPIO           EXT_IO22         inout         B7         EXT_IO_VCC         B2B, RGPIO           EXT_IO23         inout         B13         EXT_IO_VCC         B2B, RGPIO           EXT_IO24         inout         A18         EXT_IO_VCC         B2B, RGPIO           EXT_IO25         inout         A5         EXT_IO_VCC         B2B, RGPIO           EXT_IO26         inout         A13         EXT_IO_VCC         B2B, RGPIO           EXT_IO27         inout         A13         EXT_IO_VCC         B2B, RGPIO           EXT_IO28         inout         A6         EXT_IO_VCC         B2B, RGPIO           EXT_IO30         inout         A27         EXT_IO_VCC         B2B, RGPIO           EXT_IO31         inout         B15         EXT_IO_VCC         B2B, RGPIO <td< td=""><td></td><td></td><td></td><td></td><td></td></td<>					
EXT_IO19         inout         A20         EXT_IO_VCC         B2B, RGPIO           EXT_IO2         inout         B24         EXT_IO_VCC         B2B, RGPIO           EXT_IO20         inout         B14         EXT_IO_VCC         B2B, RGPIO           EXT_IO21         inout         A8         EXT_IO_VCC         B2B, RGPIO           EXT_IO22         inout         B7         EXT_IO_VCC         B2B, RGPIO           EXT_IO23         inout         B13         EXT_IO_VCC         B2B, RGPIO           EXT_IO24         inout         A18         EXT_IO_VCC         B2B, RGPIO           EXT_IO25         inout         A5         EXT_IO_VCC         B2B, RGPIO           EXT_IO26         inout         B4         EXT_IO_VCC         B2B, RGPIO           EXT_IO27         inout         A13         EXT_IO_VCC         B2B, RGPIO           EXT_IO28         inout         A17         EXT_IO_VCC         B2B, RGPIO           EXT_IO3         inout         A27         EXT_IO_VCC         B2B, RGPIO           EXT_IO30         inout         B12         EXT_IO_VCC         B2B, RGPIO           EXT_IO32         inout         A16         EXT_IO_VCC         B2B, RGPIO <td< td=""><td></td><td></td><td></td><td></td><td></td></td<>					
EXT_IO2         inout         B24         EXT_IO_VCC         B2B, RGPIO           EXT_IO20         inout         B14         EXT_IO_VCC         B2B, RGPIO           EXT_IO21         inout         A8         EXT_IO_VCC         B2B, RGPIO           EXT_IO22         inout         B7         EXT_IO_VCC         B2B, RGPIO           EXT_IO23         inout         A18         EXT_IO_VCC         B2B, RGPIO           EXT_IO24         inout         A18         EXT_IO_VCC         B2B, RGPIO           EXT_IO25         inout         A5         EXT_IO_VCC         B2B, RGPIO           EXT_IO26         inout         B4         EXT_IO_VCC         B2B, RGPIO           EXT_IO27         inout         A13         EXT_IO_VCC         B2B, RGPIO           EXT_IO28         inout         A17         EXT_IO_VCC         B2B, RGPIO           EXT_IO29         inout         A6         EXT_IO_VCC         B2B, RGPIO           EXT_IO3         inout         B5         EXT_IO_VCC         B2B, RGPIO           EXT_IO31         inout         B12         EXT_IO_VCC         B2B, RGPIO           EXT_IO32         inout         A16         EXT_IO_VCC         B2B, RGPIO           E					
EXT_IO20         inout         B14         EXT_IO_VCC         B2B, RGPIO           EXT_JO21         inout         A8         EXT_IO_VCC         B2B, RGPIO           EXT_JO22         inout         B7         EXT_JO_VCC         B2B, RGPIO           EXT_JO23         inout         B13         EXT_JO_VCC         B2B, RGPIO           EXT_JO24         inout         A18         EXT_JO_VCC         B2B, RGPIO           EXT_JO25         inout         A5         EXT_JO_VCC         B2B, RGPIO           EXT_JO26         inout         B4         EXT_JO_VCC         B2B, RGPIO           EXT_JO27         inout         A13         EXT_JO_VCC         B2B, RGPIO           EXT_JO28         inout         A17         EXT_JO_VCC         B2B, RGPIO           EXT_JO29         inout         A6         EXT_JO_VCC         B2B, RGPIO           EXT_JO3         inout         A27         EXT_JO_VCC         B2B, RGPIO           EXT_JO30         inout         B5         EXT_JO_VCC         B2B, RGPIO           EXT_JO32         inout         A16         EXT_JO_VCC         B2B, RGPIO           EXT_JO34         inout         A7         EXT_JO_VCC         B2B, RGPIO           E					
EXT_JO21         inout         A8         EXT_JO_VCC         B2B, RGPIO           EXT_JO22         inout         B7         EXT_JO_VCC         B2B, RGPIO           EXT_JO23         inout         B13         EXT_JO_VCC         B2B, RGPIO           EXT_JO24         inout         A18         EXT_JO_VCC         B2B, RGPIO           EXT_JO25         inout         A5         EXT_JO_VCC         B2B, RGPIO           EXT_JO26         inout         B4         EXT_JO_VCC         B2B, RGPIO           EXT_JO27         inout         A13         EXT_JO_VCC         B2B, RGPIO           EXT_JO28         inout         A17         EXT_JO_VCC         B2B, RGPIO           EXT_JO29         inout         A6         EXT_JO_VCC         B2B, RGPIO           EXT_JO30         inout         A27         EXT_JO_VCC         B2B, RGPIO           EXT_JO30         inout         B12         EXT_JO_VCC         B2B, RGPIO           EXT_JO32         inout         A16         EXT_JO_VCC         B2B, RGPIO           EXT_JO33         inout         A7         EXT_JO_VCC         B2B, RGPIO           EXT_JO34         inout         A9         EXT_JO_VCC         B2B, RGPIO					
EXT_JO22         inout         B7         EXT_JO_VCC         B2B, RGPIO           EXT_JO23         inout         B13         EXT_JO_VCC         B2B, RGPIO           EXT_JO24         inout         A18         EXT_JO_VCC         B2B, RGPIO           EXT_JO25         inout         A5         EXT_JO_VCC         B2B, RGPIO           EXT_JO26         inout         B4         EXT_JO_VCC         B2B, RGPIO           EXT_JO27         inout         A13         EXT_JO_VCC         B2B, RGPIO           EXT_JO28         inout         A17         EXT_JO_VCC         B2B, RGPIO           EXT_JO29         inout         A6         EXT_JO_VCC         B2B, RGPIO           EXT_JO3         inout         A27         EXT_JO_VCC         B2B, RGPIO           EXT_JO30         inout         B5         EXT_JO_VCC         B2B, RGPIO           EXT_JO31         inout         B12         EXT_JO_VCC         B2B, RGPIO           EXT_JO32         inout         A16         EXT_JO_VCC         B2B, RGPIO           EXT_JO33         inout         A7         EXT_JO_VCC         B2B, RGPIO           EXT_JO35         inout         A15         EXT_JO_VCC         B2B, RGPIO					
EXT_IO23         inout         B13         EXT_IO_VCC         B2B, RGPIO           EXT_IO24         inout         A18         EXT_IO_VCC         B2B, RGPIO           EXT_IO25         inout         A5         EXT_IO_VCC         B2B, RGPIO           EXT_IO26         inout         B4         EXT_IO_VCC         B2B, RGPIO           EXT_IO27         inout         A13         EXT_IO_VCC         B2B, RGPIO           EXT_IO28         inout         A17         EXT_IO_VCC         B2B, RGPIO           EXT_IO29         inout         A6         EXT_IO_VCC         B2B, RGPIO           EXT_IO3         inout         A27         EXT_IO_VCC         B2B, RGPIO           EXT_IO30         inout         B5         EXT_IO_VCC         B2B, RGPIO           EXT_IO31         inout         B12         EXT_IO_VCC         B2B, RGPIO           EXT_IO32         inout         A16         EXT_IO_VCC         B2B, RGPIO           EXT_IO34         inout         B9         EXT_IO_VCC         B2B, RGPIO           EXT_IO35         inout         A15         EXT_IO_VCC         B2B, RGPIO           EXT_IO36         inout         A11         EXT_IO_VCC         B2B, RGPIO					
EXT_IO24         inout         A18         EXT_IO_VCC         B2B, RGPIO           EXT_IO25         inout         A5         EXT_IO_VCC         B2B, RGPIO           EXT_IO26         inout         B4         EXT_IO_VCC         B2B, RGPIO           EXT_IO27         inout         A13         EXT_IO_VCC         B2B, RGPIO           EXT_IO28         inout         A17         EXT_IO_VCC         B2B, RGPIO           EXT_IO29         inout         A6         EXT_IO_VCC         B2B, RGPIO           EXT_IO3         inout         A27         EXT_IO_VCC         B2B, RGPIO           EXT_IO30         inout         B5         EXT_IO_VCC         B2B, RGPIO           EXT_IO31         inout         B12         EXT_IO_VCC         B2B, RGPIO           EXT_IO32         inout         A16         EXT_IO_VCC         B2B, RGPIO           EXT_IO33         inout         A7         EXT_IO_VCC         B2B, RGPIO           EXT_IO34         inout         B9         EXT_IO_VCC         B2B, RGPIO           EXT_IO35         inout         A15         EXT_IO_VCC         B2B, RGPIO           EXT_IO36         inout         A15         EXT_IO_VCC         B2B, RGPIO					
EXT_IO25         inout         A5         EXT_IO_VCC         B2B, RGPIO           EXT_IO26         inout         B4         EXT_IO_VCC         B2B, RGPIO           EXT_IO27         inout         A13         EXT_IO_VCC         B2B, RGPIO           EXT_IO28         inout         A17         EXT_IO_VCC         B2B, RGPIO           EXT_IO29         inout         A6         EXT_IO_VCC         B2B, RGPIO           EXT_IO3         inout         A27         EXT_IO_VCC         B2B, RGPIO           EXT_IO30         inout         B5         EXT_IO_VCC         B2B, RGPIO           EXT_IO31         inout         B12         EXT_IO_VCC         B2B, RGPIO           EXT_IO32         inout         A16         EXT_IO_VCC         B2B, RGPIO           EXT_IO33         inout         A7         EXT_IO_VCC         B2B, RGPIO           EXT_IO34         inout         B9         EXT_IO_VCC         B2B, RGPIO           EXT_IO35         inout         A15         EXT_IO_VCC         B2B, RGPIO           EXT_IO36         inout         A11         EXT_IO_VCC         B2B, RGPIO		inout	A18		B2B, RGPIO
EXT_IO26         inout         B4         EXT_IO_VCC         B2B, RGPIO           EXT_IO27         inout         A13         EXT_IO_VCC         B2B, RGPIO           EXT_IO28         inout         A17         EXT_IO_VCC         B2B, RGPIO           EXT_IO29         inout         A6         EXT_IO_VCC         B2B, RGPIO           EXT_IO3         inout         A27         EXT_IO_VCC         B2B, RGPIO           EXT_IO30         inout         B5         EXT_IO_VCC         B2B, RGPIO           EXT_IO31         inout         B12         EXT_IO_VCC         B2B, RGPIO           EXT_IO32         inout         A16         EXT_IO_VCC         B2B, RGPIO           EXT_IO33         inout         A7         EXT_IO_VCC         B2B, RGPIO           EXT_IO34         inout         B9         EXT_IO_VCC         B2B, RGPIO           EXT_IO35         inout         A15         EXT_IO_VCC         B2B, RGPIO           EXT_IO36         inout         A11         EXT_IO_VCC         B2B, RGPIO           EXT_IO37         inout         A11         EXT_IO_VCC         B2B, RGPIO					
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		inout	A11		B2B, RGPIO
		inout			B2B, RGPIO

EXT_IO39	inout	B16	EXT_IO_VCC	B2B, RGPIO
EXT_IO4	inout	B20	EXT_IO_VCC	B2B, RGPIO
EXT_IO40	inout	A21	EXT_IO_VCC	B2B, RGPIO
EXT_IO5	inout	A31	EXT_IO_VCC	B2B, RGPIO
EXT_IO6	inout	B23	EXT_IO_VCC	B2B, RGPIO
EXT_IO7	inout	A26	EXT_IO_VCC	B2B, RGPIO
EXT_IO8	inout	A25	EXT_IO_VCC	B2B, RGPIO
EXT_IO9	inout	A30	EXT_IO_VCC	B2B, RGPIO
FPGA_CPLD1	in	A40	3.3V	FPGA AB19, RGPIO CLK
FPGA_CPLD2	out	B28	3.3V	FPGA AB20, RGPIO out
FPGA_CPLD3	in	A38	3.3V	FPGA AD20, RGPIO in
FPGA_CPLD4	in	A36	3.3V	FPGA AE20 goes to LED2
JTAGENB	in	B30	3.3V	Enable CPLD JTAG access, otherwise M is used as GPIO
LED2	out	B10	EXT_IO_VCC	Status LED D1 red
M_TCK	in	A45	3.3V	JTAG if JTAGENB is high/ currently_not_used
M_TDI	in	A47	3.3V	JTAG if JTAGENB is high/ currently_not_used
M_TDO	out	A48	3.3V	JTAG if JTAGENB is high/ currently_not_used
M_TMS	in	B34	3.3V	JTAG if JTAGENB is high/ currently_not_used
MIO14	out	A44	3.3V	UART out to FPGA
MIO15	in	A42	3.3V	UART in from FPGA
nRST_IN	in	A32	3.3V	Reset from B2B to PS_POR
PG_ALL	in	A46	3.3V	Status power
PROG_B	in	B25	3.3V	Status PROG_B/ currently_not_used
PS_POR	inout	A41	3.3V	open drain as second reset from nRSR_IN/ currently_not_used
NC		B29	3.3V	dummy pin / not connected
NC		B27	3.3V	not connected
NC		A34	3.3V	not connected

# **Functional Description**

### **JTAG**

Set JTAGENB(J3-136) high to get access to CPLD via JTAG, otherwise CPLD JTAG Pins can be used as GPIO.

### **Power**

EN\_1V is set to constant high.

### **Boot Mode**

CPLD\_GPIO3 (J2-16) is used to set boot Mode Pin BM2\_MIO4. Signal is inverted to be compatible with second XMOD on TEBT0782

J2-16	Description		
low	SD Boot*		
high	QSPI (default)		

<sup>\*</sup> not supported with TEBT0782

### Reset

nRST\_IN drive POR\_B as open drain.

U27(TPS3106) or nRST\_IN can reset Zynq.

### **UART**

MIO14 is connected to CONFIGX.

BOOTMODE is connected to MIO15.

## **RGPIO** (beta)

RGPIO Master is a 32Bit Remote GPIO Interface to talk with FPGA over 3 lanes. System need RGPIO IP on FPGA side.

- RGPIO CLK is FPGA\_CPLD1 (up to 50MHz).
  Output is FPGA\_CPLD2
  Input is FPGA\_CPLD3

RGPIO from FPGA	Description
019	Connected to EXT_IO(even numbers), if RGPIO is activated, otherwise EXTIO is high impedance
2023	Connected to RGPIO 2023, if RGPIO is activated.
2427	Reserved
2831	Activation code from FPGA. Must match "1010"

RGPIO to FPGA	Description
019	Connected to EXT_IO(odd numbers)
2023	RGPIO 2023 from FPGA, if RGPIO is activated, otherwise zero
2427	Reserved
2831	Activation code to FPGA. Must match "1010"

### **LED**

LED2 D1 Red					
Priority	Blink Sequence	Comment			
1	******	PG_ALL, Power problem			
2	*****000	PROG_B, SoC PROGAM_B down			

3	****0000	PS_POR, SoC PS_POR_B down
4	***00000	DONE, SoC DONE down
5	user defined	FPGA_CPLD4 connected to LED

# Appx. A: Change History and Legal Notices

# **Revision Changes**

# **Document Change History**

To get content of older revision got to "Change History" of this page and select older document revision number.

Date	Document Revision	CPLD Firmware Revision	Supported PCB Revision	Authors	Description
		REV01	REV01		typo correction
					Correction
	Error rendering macro 'page-info'			Error	
	Ambiguous method overloading for			render	
	method jdk.			ing	
	proxy279.\$Proxy4022#hasContentLevel			macro	
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### **Data Privacy**

Please also note our data protection declaration at https://www.trenz-electronic.de/en/Data-protection-Privacy

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### **Environmental Protection**

To confront directly with the responsibility toward the environment, the global community and eventually also oneself. Such a resolution should be integral part not only of everybody's life. Also enterprises shall be conscious of their social responsibility and contribute to the preservation of our common living space. That is why Trenz Electronic invests in the protection of our Environment.

### REACH, RoHS and WEEE

**REACH** 

Trenz Electronic is a manufacturer and a distributor of electronic products. It is therefore a so called downstream user in the sense of REACH. The products we supply to you are solely non-chemical products (goods). Moreover and under normal and reasonably foreseeable circumstances of application, the goods supplied to you shall not release any substance. For that, Trenz Electronic is obliged to neither register nor to provide safety data sheet. According to present knowledge and to best of our knowledge, no SVHC (Substances of Very High Concern) on the Candidate List are contained in our products. Furthermore, we will immediately and unsolicited inform our customers in compliance with REACH - Article 33 if any substance present in our goods (above a concentration of 0,1 % weight by weight) will be classified as SVHC by the European Chemicals Agency (ECHA).

#### RoHS

Trenz Electronic GmbH herewith declares that all its products are developed, manufactured and distributed RoHS compliant.

#### WEEE

Information for users within the European Union in accordance with Directive 2002/96/EC of the European Parliament and of the Council of 27 January 2003 on waste electrical and electronic equipment (WEEE).

Users of electrical and electronic equipment in private households are required not to dispose of waste electrical and electronic equipment as unsorted municipal waste and to collect such waste electrical and electronic equipment separately. By the 13 August 2005, Member States shall have ensured that systems are set up allowing final holders and distributors to return waste electrical and electronic equipment at least free of charge. Member States shall ensure the availability and accessibility of the necessary collection facilities. Separate collection is the precondition to ensure specific treatment and recycling of waste electrical and electronic equipment and is necessary to achieve the chosen level of protection of human health and the environment in the European Union. Consumers have to actively contribute to the success of such collection and the return of waste electrical and electronic equipment. Presence of hazardous substances in electrical and electronic equipment results in potential effects on the environment and human health. The symbol consisting of the crossed-out wheeled bin indicates separate collection for waste electrical and electronic equipment.

Trenz Electronic is registered under WEEE-Reg.-Nr. DE97922676.

#### Error rendering macro 'page-info'

Ambiguous method overloading for method jdk.proxy279.\$Proxy4022#hasContentLevelPermission. Cannot resolve which method to invoke for [null, class java.lang.String, class com.atlassian.confluence.pages.Page] due to overlapping prototypes between: [interface com. atlassian.confluence.user.ConfluenceUser, class java.lang.String, class com.atlassian.confluence.core.ContentEntityObject] [interface com.atlassian.user.User, class java.lang.String, class com.atlassian.confluence.core.ContentEntityObject]