3.5 x 7.3 SoM differences

Feature	TE0725	TE0725LP-2C (*-72C-1)	TE0725LP-2D/2L (*-72C-1T/*-72C-1U)	Notes
Main Power VIN	3.3V	3.3V	1.8V	Special variants of TE0725LP can operate from single 1.8V supply
POF Adapter	optional	not supported	not supported	there is extra pin header on TE0725LP
Config bank Voltage	3.3V	1.8V	1.8V	
JTAG Voltage	3.3V	1.8V	1.8V	
HyperRAM	optional	optional	optional	
SPI Flash Voltage	3.3V	1.8V	1.8V	
SPI Flash Type	S25FL256S	N25Q256A	N25Q256A	Standard option
B34	42 I/O	42 I/O	42 I/O	User supplied VCCIO
B35	42 I/O	42 I/O	42 I/O	User supplied VCCIO
B14	2 I/O 3.3V	2 I/O 1.8V	2 I/O 1.8V	in JTAG/UART Header
B14	0	4 diff. I/O 1.8V	4 diff. I/O 1.8V	4 Differential pairs in extra header J3
System Clock	100MHz	25MHz	25MHz	Can be customized on request

Note:

- TE0725LP-2C includes extra 1.8V DCDC Converter, other assembly variants (TE0725LP-2D/2L) use VIN=1.8V directly to supply 1.8V circuitry.
- (*) new article names TE0725LP-xx-*