Verilog Tetris

This is very simple tetris game using student level simple code in verilog from sources found from github. The original sources have been modified to add flip flop stage in the RGB output to reduce flicker, otherwise they are used in original version.

Hardware Features used by this design

- VGA Output
- User Buttons
- 7 Segment LED
- Software Reset Button

Download: TEI0009 "Verilog Tetris"