TEI0001 Reference Designs

Description for the newest Design Version

- TEI0001 Test Board
  - Quartus Prime Lite 22.1std
  - Nios V/m
  - UART
  - Flash memory
  - SDRAM controller (AXI4)
    - Source code: https://github.com/ultraembedded/core_sdram_axi4
  - 3-axis Accelerometer
  - User LEDs
  - User buttons

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