

TE0802 Reference Designs

Description for the newest Design Version

Reference Designs	Demo Designs	Tutorial Designs
<ul style="list-style-type: none">• TE0802 Test Board<ul style="list-style-type: none">◦ Vitis/Vivado 2022.2◦ PetaLinux◦ SD◦ ETH◦ USB◦ I2C◦ Display Port (DP)◦ VGA◦ DIPS, LEDs, Buttons◦ Audio◦ MAC from EEPROM◦ Modified FSBL for Resets	<ul style="list-style-type: none">• --	<ul style="list-style-type: none">• TE0802 test board Vitis AI Tutorial<ul style="list-style-type: none">◦ Xilinx 2021.2 tools, Vivado 2021.2.1◦ Vitis AI 2.0◦ Vitis custom extensible platform for cg02_1gb board◦ Vector addition on cg02_1gb board• TE0802-02-1AEV2-A test board Vitis AI Tutorial<ul style="list-style-type: none">◦ Xilinx 2021.2 tools, Vivado 2021.2.1◦ Vitis AI 2.0◦ Vitis custom extensible platform for cg02_1gb board◦ Vector addition on cg02_1gb board <p>Special Tutorial for ZU1 variant with limited Vitis AI support</p>

Basic Documentation and Notes

- [AMD Development Tools](#)
 - [TE Board Part Files](#)
 - [TE Board Part Installation Options](#)
- [PetaLinux KICKstart](#)
- [TE Reference Design - Project Delivery](#)

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