

TEI0015 Getting Started

Overview

TEI0015

red
Status
LED

JTAG / 245 FIFO
via micro USB2

green
Power
LED

Reset
Button

User
Button

8 x red
User
LED

AREF

A0

A1

A2

A3

A4

A5

A6

D0

D1

D2

D3

D4

D5

A7

GND

AIN

GND

JTAGEN

TCK

TDO

TDO

TMS

OUT 5V

VIN 5V

OUT 3.3V

GND

Reset

D14

D13

D12

D11

D10

D9

D8

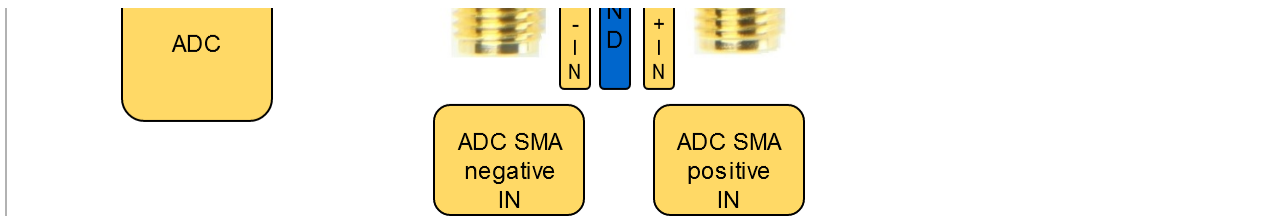
D7

D6

User I/O:
- 8 x analog
- 14 x digital

JTAG





Power Supply

- Over Micro USB or VIN Pin (5V) possible.

Buttons

- Reset Button: reset MAX10.
- User Button: FPGA Bank 8 (E6)

LED

- See overview picture.

JTAG/245 FIFO

- JTAG (first FTDI port) and 245 FIFO (second FTDI port) is over MicroUSB connector possible. External JTAG Programmer is not needed.

Reference Designs

- [TEI0015 Reference Designs](#)

Notes

- [TEI0015 Resources](#)