

# Tutorials

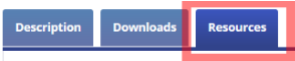
## Trenz Electronic Board

### General Usage of development tools and designs

|                   | AMD                                                                                     | Intel                                                           | Microchip | Lattice | Gowin | Colonge Chip |
|-------------------|-----------------------------------------------------------------------------------------|-----------------------------------------------------------------|-----------|---------|-------|--------------|
| Tools             | <a href="#">AMD Development Tools</a>                                                   | <a href="#">Intel Development Tools</a>                         | tbd       | tbd     | tbd   | tbd          |
| Project Delivery  | <a href="#">Project Delivery - AMD devices</a>                                          | <a href="#">Project Delivery - Intel devices</a>                | tbd       | tbd     | tbd   | tbd          |
| Reference Designs | <a href="#">TE Reference Designs Overview for Xilinx device</a>                         | <a href="#">TE Reference Designs Overview for Intel devices</a> |           |         |       |              |
| Tutorials         | <a href="#">Vitis AI and Vitis Acceleration Tutorials with Trenz Electronic Modules</a> |                                                                 |           |         |       |              |

### Series specific reference designs and other documentation

Reference designs and corresponding descriptions are available over the product pages.



Select the Resources TAB( ) on the product page and follow the links.

## Third Party FPGA Board

- [Einstieg in Digilent's NetFPGA SUME](#)
- [Getting started with Digilent NetFPGA SUME](#)
- [Vitis AI and Vitis Acceleration Tutorials with Trenz Electronic Modules](#)