

CR00140 CPLD Firmware

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Available CPLD Firmware

- [CR00140 SC CPLD MAX10](#) - Firmware description
 - Default delivered Firmware

CPLD Access

1. Place CR00140 on CRUVI carrier.
2. Make sure that JTAGEN pin on J9-57 is NOT driven low by the carrier.
3. Use TEI0004 ARROW USB Programmer2 and connect to J10, or connect signals: TCK, TDO, TMS, TDI, Ref. I/O-voltage (3.3V), GND to your quartus compatible programmer.
4. Connect programmer via usb to your PC.
5. Power up carrier, so that +3.3V_D is powered by 3.3V.

General instructions

MAX 10 Firmware Update - General Requirements

- Quartus Prime software, available for free (lite version) on <https://www.intel.de/>
- [Arrow USB Programmer Driver](#)
- Quartus compatible JTAG programmer, for example:
 - TEI0004 - Arrow USB Programmer2 or carrier board with FTDI for JTAG
 - Other JTAG Programmer compatible with Quartus Programmer
- JTAG must be connected to FPGA JTAG
- Correct Firmware (*.pof file) from Download area

MAX 10 Firmware Update - General Procedure

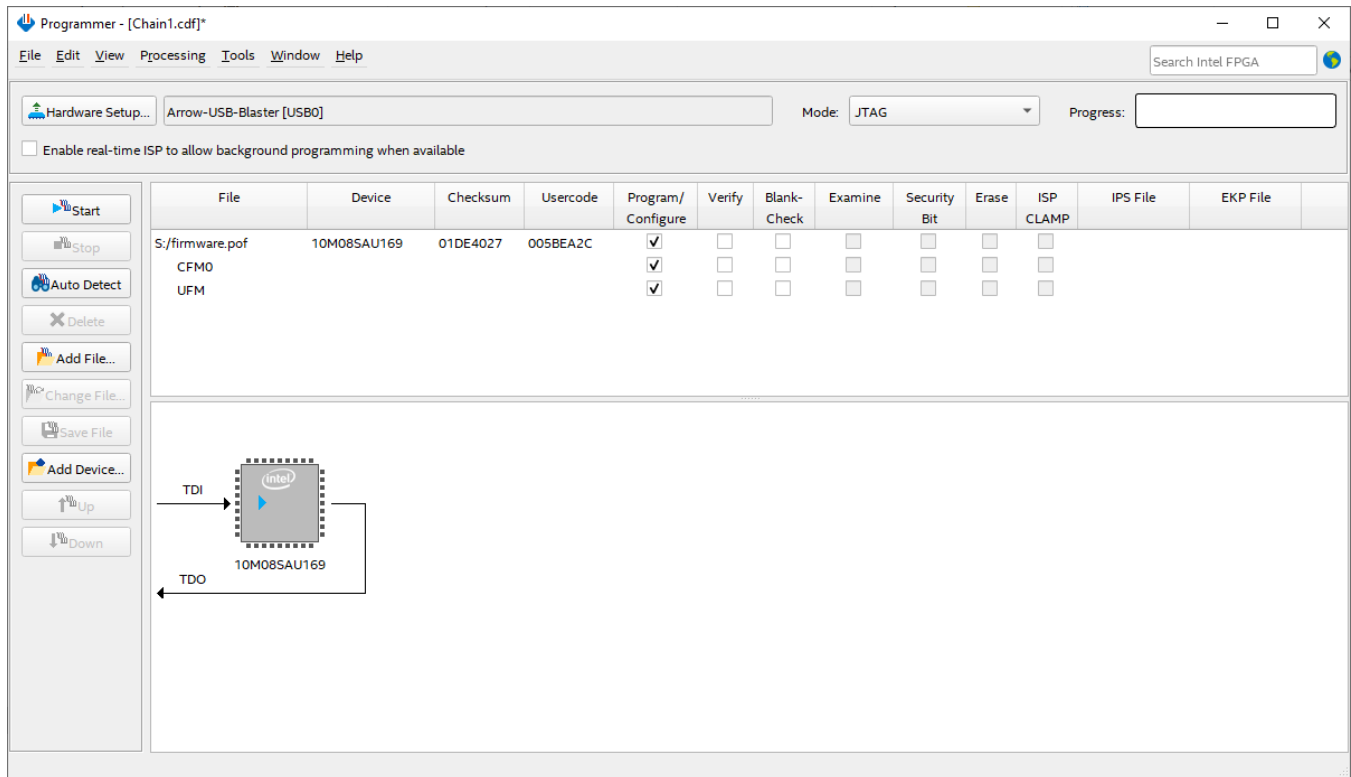
Important:

- Connect only one JTAG device to host PC.
- Close all other JTAG programs.

Procedure:

1. **Enable FPGA JTAG access** (see JTAG section on Firmware description)
2. Connect JTAG
3. Power on system
4. Open Quartus Prime
5. Open Quartus Programmer from top menu: *Tools Programmer*
6. Select from Programmer top menu: *Edit Hardware Setup*
7. Select via the drop down menu: Arrow-USB-Blaster [USB0] (Installation of [Arrow USB Programmer](#) needed) and close the window

8. Click *Add File...*
9. Select correct Firmware from Download area and press *Open*
10. Click *Start* to program the device



More information are available on the Firmware description.